JEITA

Standard of Japan Electronics and Information Technology Industries Association

EIAJ ED-7311-18

Standard of integrated circuits package (P-ILGA)

Established in March, 2002

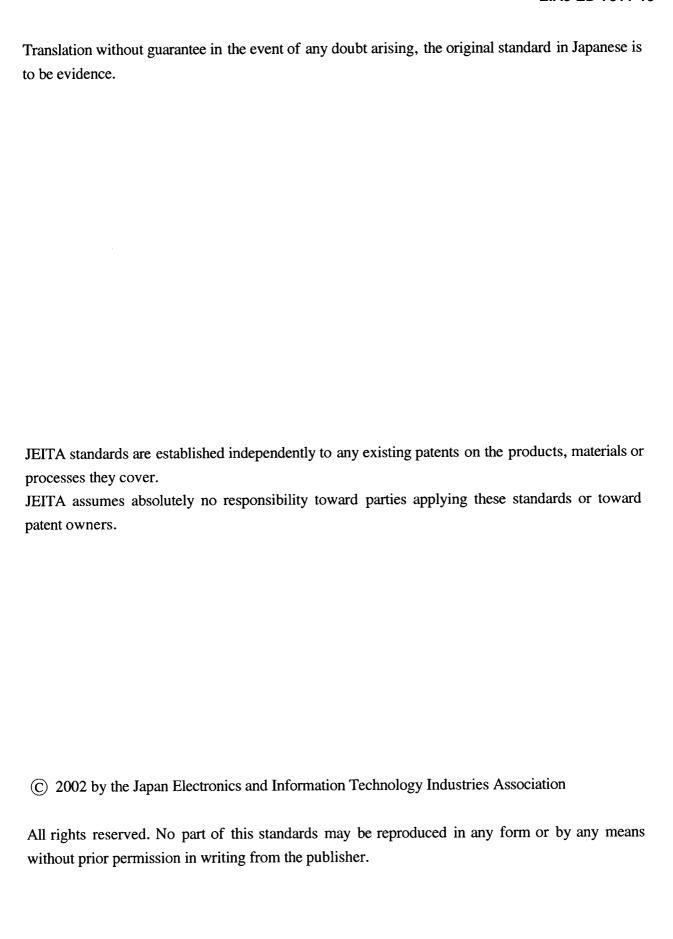
Prepared by

Technical Standardization Committee on Semiconductor Device Package

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Standard of Japan Electronics and Information Technology Industries Association

Standard of integrated circuits package (P-ILGA)

1. Scope of Application

This standard regulated outline drawings and dimensions of Plastic Interstitial Land Grid Array (herein after referred to as P-ILGA), especially plastic package, classified primary as form A and secondary as "terminal-N" under the **EIAJ ED-7300** (Recommended practice on standard for the preparation of outline drawings of semiconductor packages).

2. Terminology

The definition of the terms used in this technical report complies with the **EIAJ ED-7300** and **EIAJ EDR-7329** [Design guideline of integrated circuits for Plastic Interstitial Land Grid Array package (P-ILGA)].

3. History

Recently, electronic appliances become smaller, conventional leaded type packages such as SOP and QFP become unsuitable, and demand for no-lead type packages makes suppliers develop and commercialize such type of packages. These design guideline were in intended to standardize the outer dimensions or "terminal-N" packages and ensure compatibility between products. For the integration of definitions about dimensions or packages, which have leads on both sides and around four sides, the packages were overviewed when the design guideline was made. **EIAJ EDR-7318** [Design guideline of integrated circuits for Plastic Very Small Outline Non-Leaded Package(P-VSON)] was established in December, 1998, which have leads on both sides, and **EIAJ EDR-7324**[Design guideline of integrated circuits for Plastic Very thin Quad Flat Non-leaded package(P-VQFN)] was established in April, 1999, which have leads around four sides.

EIAJ EDR-7329 places witch the derivation package of **EIAJ EDR-7324**. And this package has terminals, which newly, zigzag (staggered) terminal type witch P-ILGA. It began a discussion from January, 2001, and establishment schedule in March, 2002. This standard shows the standard design values on the concept of the design centers as far as possible for standardization.

4. Definition of P-ILGA

It is classified into N terminals of second category having the form of D. It's leads are flat and positioned at the bottom around four sides or the package to make it possible to mount on the printed circuit board (Metal exposing area is not defined in this report). Incidentally, "I" is the initial of Interstitial, Interstitial terminal of the package means a package except the terminal which stood in line in series.

5. Numbering of Pins

In conformity with the definition of EIAJ ED-7300.

6. Nominal Dimensions

The package body size (package length: D, package width: E) is regarded as Nominal dimensions.

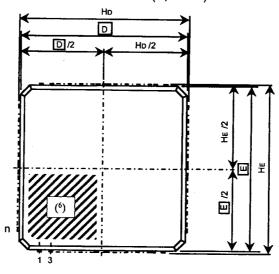
照合文字と図面

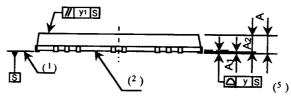
REFERENCE CHARACTERS AND DRAWING

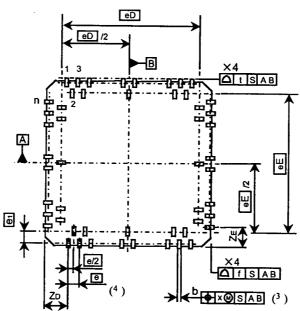
外形図

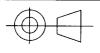
Outline Drawing

図 1, Figure 1 ($L_1=0.10$)







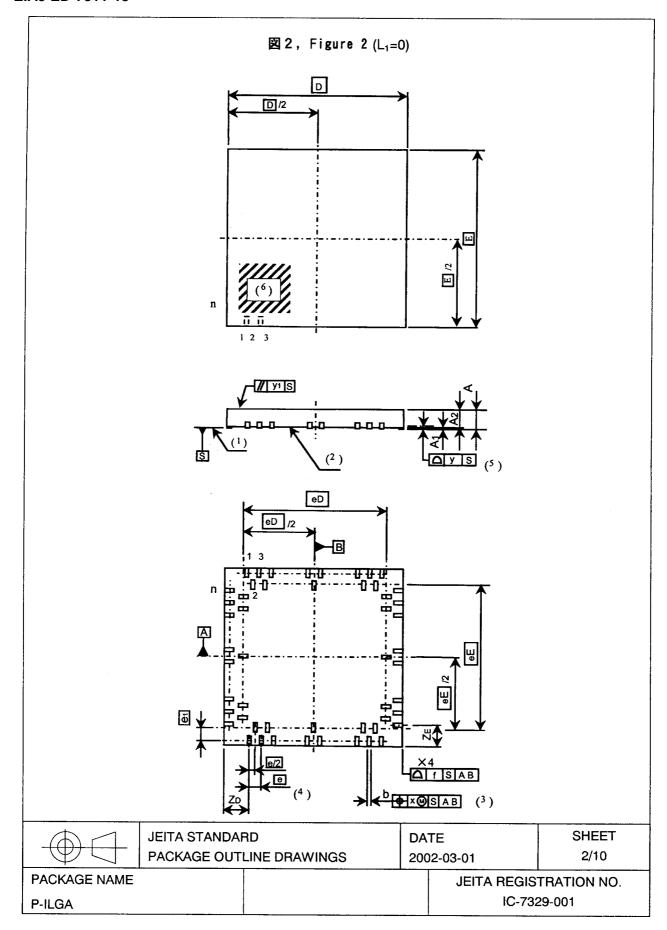


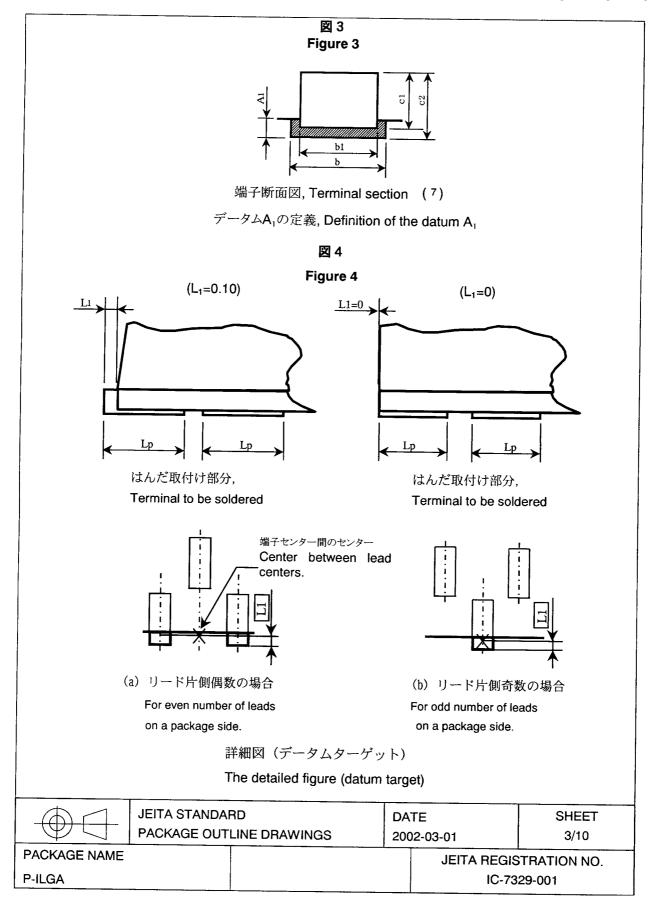
JEITA STANDARD
PACKAGE OUTLINE DRAWINGS

DATE 2002-03-01 SHEET 1/10

PACKAGE NAME P-ILGA

JEITA REGISTRATION NO. IC-7329-001





EIAJ ED-7311-18

- **注 (¹)** 取付け面を示す。取付け面とは、パッケージを取付ける面に対して、パッケージが接触 し合う面である。
 - (2) ベース面を示す。ベース面とは、取付け面に平行な本体の最下点を通る面をいう。 但し、スタンドオフは除く。
 - (3) 最大実体条件(ISO 2692/JIS B 0023参照)は、端子の位置許容値に適用する。
 - (4) 端子の軸の真の幾何学位置を規定する。
 - (5) 各端子の平坦部の取付け面に対する鉛直方向のずれを規定する。
 - (6) インデックスマークの許容位置を示す。インデックスマークは、IEC規格に準拠し、ボディサイズの1/16を基本とし、ボディサイズが小さい場合でもパッケージ1/4より少い面積の斜線領域内にその面積の全てが包含されなければならない。
 - (7) 端子断面の寸法は、端子先端から0.10mmと0.25mmの間の端子存在範囲内に適用する。

Notes

- (1) The mounting surface, with which a package is in contact.
- (2) The base surface, which is in parallel with the mounting surface and links the lowest point, except the stand-off.
- (3) The maximum mounting conditions apply to the positional tolerance of the terminals. (Refer to ISO 2692/JIS B 0023.)
- (4) Specifies the true geometric position of the terminal axis.
- (5) Specifies the vertical shift of the flat part of each terminal form the mounting surface.
- (6) Shows the allowable position of the Index mark area, which is based on the IEC standard, basically 1/16 with package bodysize, however in case of small body size, it is less than package 1/4 with package bodysize, it must be included in the shaded area entirely.
- (7) The dimensions of the terminal section apply to the terminal region ranges of 0.10mm and 0.25mm from the end of a terminal.

	JEITA STANDAI PACKAGE OUT	RD LINE DRAWINGS	DA 200	TE)2-03-01	SHEET 4/10
PACKAGE NAME					TRATION NO.
P-ILGA				IC-73	29-001

登録一覧表,Registration table

パッケージ名称 (Package name):P-ILGA

登録番号 (Registration number): IC-7329-001

·整理番号表, serial number table

注 表中の数値は、(整理番号)の1桁目から4桁目を示す。

Note The Numbers in the table indicate, (serial number) of 1st – 4th cord

CODE	1st	2nd		3rd		4th	
symbol	A	D		E		е	
	Т	3	Α	3	Α	1.00	Α
	V	4	В	4	В	0.80	В
	W	5	С	5	С	0.65	С
	U	6	D	6	D	0.50	D
	Х	7	E	7	E	0.40	Ε
		8	F	8	F	****	
		10	G	10	G		
	İ	12	Н	12	Н		
		14	J	14	J		
		16	K	16	К		
		18	Ĺ	18	L		
		20	М	20	М		

注 表中の数値は、「(端子数 n)-001-(整理番号)」を示す。

Note The Numbers in the table indicate,

(Terminal number n) - 001 - (serial number)

Lр		0.55	0.	30	
e	1.00	0.80	0.65	0.50	0.40
3×3					
4×4					
5×5					
6×6	*****				
7×7	44-001-WEEA	52-001-WEEB	60-001-WEEC		
8×8	52-001-VFFA	60-001-VFFB	76-001-VFFC	-	
10×10	60-001-VGGA	76-001-VGGB	100-001-VGGC		
12×12	76-001-VHHA	100-001-VHHB	124-001-VHHC		
14×14	92-001-VJJA	116-001-VJJB	148-001-VJJC		
16×16					
18×18					
20×20					

$\bigcirc \bigcirc$	JEITA STANDAI PACKAGE OUT	RD LINE DRAWINGS	DA 200	TE 02-03-01	SHEET 5/10
PACKAGE NAME				JEITA REGIS	TRATION NO.
P-ILGA	<u>.</u>			IC-73	29-001

Unit mm 整理番号 44-001-WEEA 52-001-WEEB 60-001-WEEC Serial Number 外形タイプ P-ILGA044-07.00×07.00-1.00 P-ILGA052-07.00×07.00-0.80 P-ILGA060-07.00×07.00-0.65 External Type 照合文字 min nom max min nom max min nom max Reference Symbol D 7.00 7.00 7.00 E 7.00 7.00 _ __ 7.00 _ A_2 0.65 0.70 0.75 0.65 0.70 0.75 0.65 0.70 0.75 f 0.20 0.20 0.20 Α ---0.80 ---___ 0.80 _ ---0.80 A_1 0.00 0.00 0.00 Group1 b 0.17 __ 0.25 0.17 0.25 0.13 0.21 b, 0.17 0.20 0.23 0.17 0.20 0.23 0.13 0.16 0.19 е 1.00 0.80 0.65 e/2 0.50 0.40 0.325 e, 0.87 ___ 0.69 0.56 еD 4.92 5.26 5.52 еE 4.92 5.26 5.52 Lp 0.45 0.55 0.65 0.45 0.55 0.65 0.45 0.55 0.65 $L_{\scriptscriptstyle 1}$ 0.00 0.00 0.00 х 0.20 0.16 0.13 У 0.10 0.10 0.10 t 0.20 0.20 0.20 n 44 52 60 H_D 7.00 7.00 7.00 H_{E} 7.00 7.00 7.00 Z_{D} 1.50 Group2 1.10 1.225 Z_{ϵ} 1.50 1.10 1.225 C₁ 0.09 ___ 0.21 0.09 0.21 0.09 0.21 C_2 0.09 0.25 0.25 0.09 0.09 0.25

	JEITA STANDAI PACKAGE OUT	RD LINE DRAWINGS	DA 200	TE 02-03-01	SHEET 6/10	
PACKAGE NAME				JEITA REGIS	TRATION NO.	
P-ILGA				IC-73	29-001	

呼び寸法(Nominal dimension): 🗗 🗲, 08.00×08.00

Unit mm

		T							Unit	mm	
Serial N	整理番号 Serial Number		2-001-VF	FA	6	60-001-VFFB			0-001-VFI	FC	
外形的 Externa		P-ILGA052-08.00×08.00-1.00			P-ILGA0	P-ILGA060-08.00×08.00-0.80			P-ILGA076-08.00×08.00-0.65		
照合文字 Reference Symbol		min	nom	max	min	nom	max	min	nom	max	
	D		8.00			8.00	_	_	8.00	_	
	E	_	8.00	_	_	8.00		_	8.00	_	
	A ₂	0.70	0.75	0.80	0.70	0.75	0.80	0.70	0.75	0.80	
	f		_	0.20		_	0.20	_	_	0.20	
	Α			0.85		_	0.85		_	0.85	
_	Α,	0.00			0.00			0.00			
Group1	b	0.17		0.25	0.17		0.25	0.13	_	0.21	
Grc	b ₁	0.17	0.20	0.23	0.17	0.20	0.23	0.13	0.16	0.19	
	е	_	1.00	_	_	0.80			0.65		
	e/2	_	0.50	_	_	0.40		_	0.325	_	
	Θ ₁		0.87	_		0.69	_		0.56		
	eD		5.92	_	_	6.26	_		6.52	_	
	eЕ		5.92			6.26	_		6.52		
	Lp	0.45	0.55	0.65	0.45	0.55	0.65	0.45	0.55	0.65	
	L ₁		0.10			0.10			0.10	_	
	х			0.20	_	_	0.16		_	0.13	
	у			0.10	_	_	0.10	_	_	0.10	
	t			0.20			0.20		_	0.20	
	n	_	52			60			76	_	
	H _D		8.20			8.20			8.20	_	
	H _E	_	8.20	_	_	8.20			8.20		
1p2	Z _D		1.00			1.20			1.075		
Group2	Z _E		1.00			1.20			1.075		
\circ	С ₁	0.09	_	0.21	0.09	_	0.21	0.09		0.21	
	c ₂	0.09		0.25	0.09	_	0.25	0.09		0.25	

	JEITA STANDAI PACKAGE OUT	RD LINE DRAWINGS	DA 200	TE 02-03-01	SHEET 7/10
PACKAGE NAM	E			JEITA REGIS	TRATION NO.
P-ILGA				IC-73	29-001

350 TH		1			r				Unit	mm
Serial I	!番号 Number	60)-001-VG(GA	76	6-001-VG	GB	10	0-001-VG	GC
	タイプ al Type	P-ILGA0	P-ILGA060-10.00×10.00-1.00			76-10.00×1	0.00-0.80	P-ILGA1	00-10.00×1	0.00-0.65
照合	文字 e Symbol	min	nom	max	min noi	nom	max	min	nom	max
	D		10.00			10.00	_	_	10.00	
	E	_	10.00	_		10.00	_	_	10.00	
	A ₂	0.70	0.75	0.80	0.70	0.75	0.80	0.70	0.75	0.80
	f		_	0.20	_		0.20		_	0.20
	А			0.85			0.85	_	_	0.85
_	Α,	0.00	_		0.00	_	_	0.00		
Group1	b	0.17	_	0.25	0.17	_	0.25	0.13		0.21
Gro	b ₁	0.17	0.20	0.23	0.17	0.20	0.23	0.13	0.16	0.19
	Ф		1.00	_	_	0.80			0.65	_
	e/2	_	0.50	_	_	0.40			0.325	
	e ₁	_	0.87	_	_	0.69	-	_	0.56	
	eD	_	7.92			8.26	_	_	8.52	_
	eЕ		7.92		_	8.26		_	8.52	_
	Lp	0.45	0.55	0.65	0.45	0.55	0.65	0.45	0.55	0.65
	L ₁		0.10	_		0.10		_	0.10	
	×		_	0.20	_	_	0.16		****	0.13
	У		_	0.10	_	_	0.10		_	0.10
	t		_	0.20	_	_	0.20	_		0.20
	n		60		_	76		_	100	
	H _D		10.20			10.20			10.20	
	H _E		10.20			10.20			10.20	
$_{ m lp} 2$	Z _D		1.50		_	1.40			1.10	
$\operatorname{Group} 2$	Z _E	-	1.50			1.40			1.10	
9	C ₁	0.09		0.21	0.09		0.21	0.09		0.21
	c ₂	0.09	_	0.25	0.09		0.25	0.09	_	0.25
) (1	- 1	STAND		RAWING	GS.	DA.	TE 02-03-01			SHEET 8/10

呼び寸法(Nominal	l dimension): DxE,	12.00×12.00
--------------	--------------------	-------------

	1 ! 4	
ι	Jnit	mm

	整理番号 Serial Number		6-001-VH	НА	10	0-001-VH	IHB	124-001-VHHC		
外形名 Externa	外形タイプ External Type		76-12.00×1	2.00-1.00	P-ILGA100-12.00×12.00-0.80			P-ILGA124-12.00×12.00-0.65		
照合: Reference		min	nom	max	min	nom	max	min	nom	max
			12.00			12.00		_	12.00	
			12.00	_		12.00			12.00	_
	A ₂	0.75	0.85	0.95	0.75	0.85	0.95	0.75	0.85	0.95
	f		_	0.20		_	0.20		_	0.20
	Α	_	_	1.00	_	_	1.00		_	1.00
1	A ₁	0.00	_		0.00			0.00	_	_
Group1	b	0.17	_	0.25	0.17	-	0.25	0.13	_	0.21
Gro	b ₁	0.17	0.20	0.23	0.17	0.20	0.23	0.13	0.16	0.19
	Ф	_	1.00		_	0.80	_		0.65	_
	e/2		0.50	_		0.40	_		0.325	_
	e ₁	_	0.87	_	_	0.69	_		0.56	
	eD	_	9.92		_	10.26	_	_	10.52	
	eЕ	_	9.92	_	_	10.26	_		10.52	_
	Lр	0.45	0.55	0.65	0.45	0.55	0.65	0.45	0.55	0.65
	L ₁	_	0.10	-		0.10	_		0.10	
	X	_	_	0.20			0.16	_	_	0.13
	У		_	0.10	_	_	0.10	_		0.10
	t	_		0.20			0.20		_	0.20
	n	_	76			100			124	
	H _D	_	12.20			12.20	_		12.20	_
	H _E	_	12.20			12.20	_	_	12.20	
p2	Z _D		1.50	_		1.20	_		1.125	
Group2	Z _E		1.50	_		1.20			1.125	
3	c ₁	0.09	_	0.21	0.09		0.21	0.09	_	0.21
	C ₂	0.09	_	0.25	0.09	_	0.25	0.09	_	0.25

	JEITA STANDAI PACKAGE OUT	RD LINE DRAWINGS	DATE 2002-03-01		SHEET 9/10	
PACKAGE NAM	E			JEITA REGIS	TRATION NO.	
P-ILGA	· · · · · · · · · · · · · · · · · · ·			IC-7329-001		

EIAJ ED-7311-18

P-ILGA

					т				Uni	t mm	
Serial N	整理番号 Serial Number		92-001-VJJA			116-001-VJJB			148-001-VJJC		
外形タイプ External Type		P-ILGA092-14.00×14.00-1.00			P-ILGA116-14.00×14.00-0.80			P-ILGA148-14.00×14.00-0.65			
照合文字 Reference Symbol		min	nom	max	min	nom	max	min	nom	max	
Helefello		_	14.00	_	_	14.00			14.00		
			14.00			14.00	_	_	14.00	_	
	A ₂	0.75	0.85	0.95	0.75	0.85	0.95	0.75	0.85	0.95	
i	f	_	_	0.20	-		0.20	<u> </u>	-	0.20	
	Α	_	_	1.00	_		1.00	_	_	1.00	
	A ₁	0.00	_	_	0.00	<u> </u>		0.00	_	_	
up1	b	0.17	_	0.27	0.17	_	0.27	0.13	_	0.23	
Group1	b ₁	0.17	0.20	0.23	0.17	0.20	0.23	0.13	0.16	0.19	
•	e	_	1.00	_		0.80	_	_	0.65	_	
	e/2	_	0.50	_	_	0.40	_	_	0.325	_	
	e ₁		0.87	_		0.69	_	_	0.56	_	
	eD	_	11.92	_	_	12.26	_	_	12.52	_	
	еE	_	11.92			12.26	_	_	12.52	_	
	Lp	0.45	0.55	0.65	0.45	0.55	0.65	0.45	0.55	0.65	
	L ₁	_	0.10	_	_	0.10	_	_	0.10		
	x		_	0.20	_		0.16	_	_	0.13	
	У			0.10	_	_	0.10	_		0.10	
	t	_		0.20	_	_	0.20	_		0.20	
	n		92	_		116	_	_	148	_	
	H _D		14.20			14.20		_	14.20		
	H _E		14.20			14.20	_		14.20		
1p2	Z _D		1.50			1.40		_	1.15		
Group2	Z _E		1.50			1.40			1.15		
	C ₁	0.09	_	0.21	0.09		0.21	0.09		0.21	
	c ₂	0.09	_	0.25	0.09		0.25	0.09	_	0.25	
	JEITA	A STANDARD			· · · · · · · · · · · · · · · · · · ·	DATE				SHEET	

IC-7329-001

COMMITTEE MEMBERS

The IC Package Subcommittee of the Technical Standardization Committee on Semiconductor Device Packages has mainly deliberated this standard.

The subcommittee members are shown below.

iization Committee on Semiconductor Device Package>			
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	ELPIDA MEMORY,INC. Subcommittee> SANYO ELECTRIC CORP. SHARP CORP. FUJITSU LTD. TOSHIBA CORP. MATSUSHITA ELECTRIC INDUSTRIAL CO.,LTD. AMKOR THECHNOROGY JAPAN. INC. ENPLAS CORP. ELPIDA MEMORY, INC. OKI ELECTRONICS INDUSTRY CO.,LTD. KYOCERA CORP. KOGUNEX SANYO ELECTRIC CORP SUMITOMO 3M CORP. SEIKO EPSON CORP. SONY CORP. TOSHIBA CORP NEC CORP. NEC CORP. IBM JAPAN CORP. TEXAS INSTRUMENTS JAPAN LTD. HITACHI LTD. HITACHI Cable LTD. FUJI ELECTRIC CO.,LTD. MATSUSHITA ELECTRIC INDUSTRIAL CO.,LTD. MITSUBISHI ELECTRIC CORP. MELCO INC. YAMAICHI ELECTRIC CO.,LTD. UNITECHNO INC. ROHM CO.,LTD. SHIN-ETSU POLYMER TOYOJUSHI CO.,LTD. MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD. NEC CORP. MELCO INC. YAMAICHI ELECTRIC CO.,LTD. WHITESUBISHI CO.,LTD. MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD. NEC CORP. MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.		