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# EIAJ EDR-7314A

# Design guideline of integrated circuits for Plastic Shrink Small Outline Package (P-SSOP)

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## Design guideline of integrated circuits for Plastic Shrink Small Outline Package (P-SSOP)

#### 1. Scope of Application

This technical report regulated outline drawings and dimensions of the Plastic Shrink Small Outline Package (herein after referred to as P-SSOP), which terminal straight pitch e is 1.00mm less than, among the packages classified as form B in the **EIAJ ED-7300** [Recommended practice on Standard for the preparation of outline drawings of semiconductor packages]

**Note** This technical report is revised and corresponds to **EIAJ ED-7311-20** [Standard of integrated circuits package (P-SSOP)] established in January 2002. The other relation standards are shown below.

EIAJ EDR-7320 [Design guideline of integrated circuits for small outline package (SOP)] established in December 1998

EIAJ ED-7311-19 [Standard of integrated circuits package (P-SOP)] established in January 2002 EIAJ EDR-7312 [Design guideline of integrated circuits for thin small outline package (type I) (TSOP I)] established in April 1996

EIAJ EDR-7313 [Design guideline of integrated circuits for thin small outline package (type II) (TSOP II)] established in April 1996

**EIAJ ED-7311-1** [Standard of integrated circuits package (TSOP(1))] established in August 1997 **EIAJ ED-7311-2** [Standard of integrated circuits package (TSOP(2))] established in August 1997

#### 2. Definition of the Technical Terms

The definition of the technical terms used in this technical report is in conformity with **EIAJ ED-7300**, and the definition of technical terms appearing a new are given within the text of this standard.

#### 3. BACKGROUND

Recently, electronic appliance become smaller and thinner, on such background it is appearance the Plastic Small Outline Package (herein after referred to as P-SOP), terminal straight pitch e is 1.27mm(50mil). And after that, Terminal straight pitch e made shrink P-SSOP also became used. This technical report intended to standardize the outer dimensions of P-SSOP and ensure compatibility between products as far as possible for standardization.

#### 4. Definition of P-SSOP

P-SSOP is defined as Form B with L terminal in the item 6, "Outline classification of shapes of semiconductor package " at the **EIAJ ED-7300**, and a package with formed terminals led out of longer side of itself in two directions, whose terminal pitch e is 1.00mm or less, are flat toward the outside of the package body for mounting on print circuits board surface. (gull wing shape lead)

#### 5. Numbering of Terminals

Numbering of terminals complies with the EIAJ ED-7300.

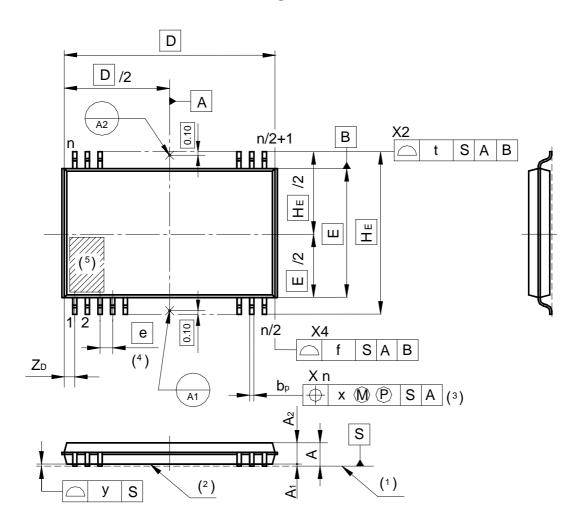
#### 6. Nominal Dimensions

Package width X Package length(Symbol :  $\begin{bmatrix} E \\ X \\ D \end{bmatrix}$ ) is applied to Nominal Dimensions. However as the exception, old nominal dimensions is possible to use places (side by side) and it writes which Package width(Symbol : E).

## 7. REFERENCE CHARACTERS AND DRAWING

## 7.1 Outline Drawing

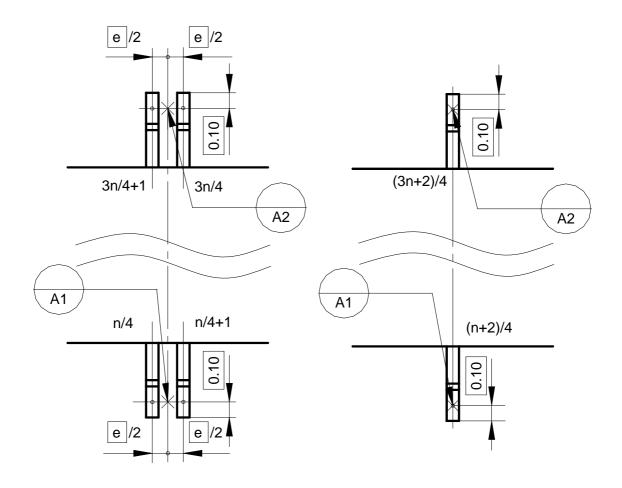
Figure 1



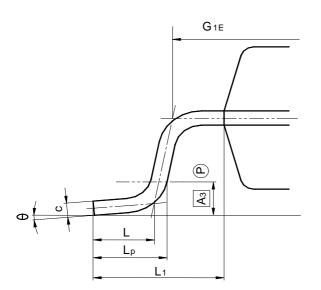
#### Figure 2 Detail area of datum A

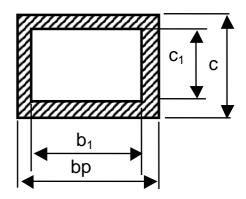
For even number of terminals on a package side

For odd number of terminals on a package side









Terminal cross section (6)

Notes (<sup>1</sup>) The mounting surface, with which a package is in contact.

- (<sup>2</sup>) The base surface, which is in parallel with the mounting surface and links the lowest point, except the stand-off.
- (<sup>3</sup>) The maximum mounting conditions apply to the positional tolerance of the terminals.
   (Refer to ISO 2692/JIS B 0023.)
- (<sup>4</sup>) Specifies the true geometric position of the terminal axis.
- (<sup>5</sup>) Shows the allowable position of the Index mark area, which is based on the IEC standard and basically 1/16 with package body size, however in case of small package body size, it is less than 1/4 with
- (<sup>6</sup>) The dimensions of the terminal section apply to the terminal region ranges of 0.10mm and 0.25mm from the end of a terminal.
- (<sup>7</sup>) Resin burrs and gate remains of the package sides with no terminals are max.0.15mm per side.
- (<sup>8</sup>) Resin burrs of the terminal sides are max.0.25mm per side.

#### 7.2 PATTERN OF TERMINAL AREAS

Pattern of terminal areas can exist is shown Figure 4 as reference for the foot print design.

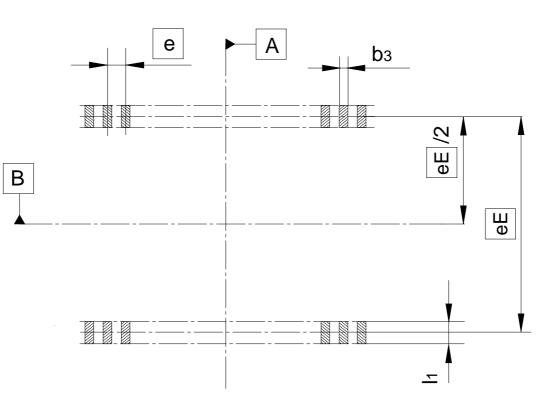


Figure 4

I1max=Lpmax+t/2b3max=bpmax+X eE = H<sub>E</sub> -Lpnom

#### 8. Outer Dimension

**Table 1** below shows the standard dimensions. Combinations of the standard dimensions shown belowallow a number of package variations. If a package is newly designed, their dimensions shall beselected in the 8.3 Table 2. Number of terminal list.

#### 8.1 GROUP 1

		l able 1		Unit: mm
Description	Reference symbol	Standards	Recommended	Remarks
Nominal dimensions	EXD	Package width( E ) X Package length( D ) is applied to Nominal Dimensions. However as the exception, old nominal dimensions is possible to use places (side by side) and it writes which Package width (Symbol : E). Old nominal dimensions E Group1 Group2 Group3 0020 0030 0044 0056 0061 (0080) 0300 (0099) 0350 0115 0130 0145 0160	Old nominal dimensions which Group 1 (0080), (0099) is non- recommendation.	<ul> <li>(1) Old nominal dimensions which Group 1, 3 are the numbers without decimal points</li> <li>(2) Group 2 of numerical value are from mil systems.</li> </ul>
Package width	E	Group1         Group2         Group3           2.00         3.00         4.40           5.60         6.10         6.10           (8.00)         7.62         9.90)         8.89           10.16         11.50         13.00           14.50         16.00         16.00	Group 1 (8.00), (9.90) is non- recommendation.	<ul> <li>(1) Mismatch of the upper and lower mold and gate remains and resin burrs are not included.</li> <li>(2) Spec of resin burrs and gate remains are expressed in notes of reference characters and drawings, (<sup>7</sup>), (<sup>8</sup>).</li> <li>(3) It is recommended that the upper and lower cavities are equal size. If they are different, the larger dimension shall be regarded as E.</li> <li>(4) Combination of E and D must be selected from the same Group (1 or 2 or 3) of each list.</li> <li>(5) Group 2 is same range of TSOP(2)</li> </ul>

#### Table 1

## Table1 (continued)

				Unit: mm
Description	Reference symbol	Standards	Recommended	Description
Package length	D	$\begin{tabular}{ c c c c c } \hline \hline D \\ \hline \hline Group1 & Group2 & Group3 \\ \hline 2.00 & & & \\ \hline 3.00 & & & \\ \hline 3.60 & & & \\ \hline 5.00 & & & \\ \hline 6.50 & & & \\ \hline 7.80 & & & \\ \hline 9.70 & 10.79 & & \\ \hline 11.00 & 12.06 & & \\ \hline 12.50 & 13.33 & 13.25 & \\ \hline 14.00 & 14.60 & 14.50 & \\ \hline (15.80) & 15.87 & 15.75 & \\ \hline (17.00) & 17.14 & 17.00 & \\ \hline (18.50) & 18.41 & 18.25 & \\ \hline (20.80) & 19.68 & 19.50 & \\ \hline 20.95 & 20.75 & \\ \hline 22.22 & 22.00 & \\ \hline 23.49 & 23.25 & \\ \hline 24.76 & 24.50 & \\ \hline 26.03 & 25.75 & \\ \hline 27.30 & 27.00 & \\ \hline 28.57 & 28.25 & \\ \hline 29.50 & & & \\ \hline 30.75 & & \\ \hline \end{tabular}$	Group1 (15.80), (17.00), (18.50), (20.80) is non- recommendation.	(1) Mismatch of the upper and lower mold and gate remains and resin burrs are not included. (2)Spec of resin burrs and gate remains are expressed in notes of reference characters and drawings, $(7)$ , $(8)$ . (3) It is recommended that the upper and lower cavities are equal size. If they are different, the larger dimension shall be regarded as D. (4) Combination of E and D must be selected from the same Group (1or2or3) of each list. (5) Group 2 is same range of TSOP(2)
Tolerance of package lateral profile	f	<ul> <li>(1) The tolerance of terminal tip shall be specified in the outline drawing.</li> <li> f S A B </li> <li>(2) Reference symbol f shall be replaced as below. D≤14.00, f = 0.10 D&gt;14.00, f = 0.20</li></ul>		S and A and B is datum.
Overall width	Hg	He = E + 2xL1	_	Hg -Lpnom= eE

## Table 1 (continued)

Description	Reference symbol	ç	Standards	3	Recommended	Remarks
Package height	A <sub>2</sub>	A2rr           Group1         0.5           0.7         0.9           1.1         1.4           1.6         1.3           Group2         1.3           Group3         1.3	55         0.6           75         0.8           95         1.0           10         1.2           10         1.5           50         1.7           30         1.4           30         1.4	30         0.65           30         0.85           30         1.05           20         1.30           30         1.60           70         1.80           40         1.50	_	<ul> <li>(1) Include package warp age.</li> <li>(2) It must be selected from the same Group (1, 2, 3) of E/D.</li> </ul>
Seated height	A	Group1 Group2 Group3	Amax 0.80 1.00 1.20 1.45 1.75 1.95 1.65 1.65 3.05	A2nom 0.60 0.80 1.00 1.20 1.50 1.70 1.40 1.40 2.70	_	<ul> <li>(1) Include package warp age.</li> <li>(2) It must be selected from the same Group (1, 2, 3) of E/D.</li> <li>(3) Especially, it shows SSOP, which is the thin seated height by the following name.</li> </ul> <u>name Amax A2nom WSSOP 0.80 0.60 VSSOP 1.00 0.80 TSSOP 1.20 1.00 LSSOP 1.70 1.40</u>
Stand-off Height	A <sub>1</sub>	A₁min A 0.05	A₁nom 0.10	A <sub>1</sub> max 0.15	Amax A1max 0.80 1.00 0.10	
Standard height of soldered points	A3	Ag = 0.25			_	

Table 1 (continued)

	r			Unit: mm
Description	Reference symbol	Standards	Recomm-ended	Remarks
Length of soldered part	Lр	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	-	(1) It must be selected from the same Group (1, 2 3) of E/D. (2) It applies the following Lp value below $\blacksquare$ =3.00mm Group1. Lpmin=0.15 Lpnom=0.30 Lpmax=0.50 In case of L=0.30, L <sub>1</sub> =0.50
Angle of terminal fiat portions	θ	θninθnomθmax0°3°8°	-	
Terminal pitch	e	e 1.00 0.50 0.80 0.40 0.65 0.30	-	
Tolerance of terminal center position	x	(1) Tolerance of terminal center position shall be specified in the outline drawing. $\begin{array}{r c c c c c c c c } \hline \hline & X & \hline \hline & P & S & A \\ \hline \hline & X & \hline & P & S & A \\ \hline \end{array}$ (2) Reference symbol x shall be replaced as below. $\begin{array}{r c c c c c c c c c c c c c c c c c c c$		<ol> <li>M means the concept of the maximum material condition (MMC) and it shall be applied.</li> <li>P is Means projected tolerance zone, it shows the range of the measurement object which guaranteeing the height of Ag</li> </ol>

## Table 1 (continued)

Description	Reference symbol		Stan	dards		Reco	mmended	Remarks		
Terminal width	bp	  	00         0.           80         0.           65         0.           50         0.           40         0.	34     0       24     0       16     0       17     0       13     0	max .50 .40 .32 .27 .23 .18	(1) Solde 	$\begin{array}{c} \text{plating} \\ b_{p}\text{nom} \\ 0.42 \\ 0.32 \\ 0.24 \\ 0.22 \\ 0.18 \\ 0.14 \\ \hline \\ \text{dium plating} \\ b_{p}\text{nom} \\ 0.40 \\ 0.30 \\ 0.22 \\ 0.20 \\ 0.16 \\ 0.12 \\ \hline \end{array}$	<ul> <li>(1) b<sub>p</sub> and c denot the terminal widt and thickness wit plating.</li> <li>b<sub>1</sub> and c<sub>1</sub> denote th terminal width an thickness befor plating.</li> <li>(2) b<sub>p</sub>,b<sub>1</sub>,c,c<sub>1</sub>apply to the ranges of 0.10-0.25 from the tip of a terminal.</li> <li>(3) The standard value of b<sub>p</sub>,c,</li> <li>Solder plating, the standard thickness of solder layer shall be, [0.010</li> </ul>		
	b1	1.00 0.80 0.65 0.50 0.40 0.30	b1min 0.34 0.24 0.16 0.17 0.13 0.09	b₁nom 0.40 0.30 0.22 0.20 0.16 0.12	b1max           0.46           0.36           0.28           0.23           0.19           0.15		_	+0.010/-0.005] As palladium plating it is very thin, so terminal width and thickness is, b <sub>pnom</sub> =b <sub>1mom</sub> C <sub>nom</sub> =C <sub>1nom</sub>		
Terminal hickness	С	<u>c₁nor</u> 0.15 0.124 0.10	0.1 5 0.11	4 0 15 0	nax .20 .17 .15	(1) Solder c1nom 0.15 0.125 0.10 (2) Palladi c1nom 0.15 0.125 0.125 0.10	plating           cnom           0.17           0.145           0.12           ium plating           cnom           0.15           0.125           0.10			
	C1	c₁min 0.14 0.115 0.09	c <sub>1</sub> nor 0.15 0.12 0.10	<u> </u>	nax 16 135 11		_			

#### Table 1 (continued)

Description	Reference symbol	Standards	Recommended	Remarks
Co planarity	у	(1) The co planarity shall be specified in the outline drawing. <b>y S</b> (2) Reference symbol y shall be replaced with any of the values shown below. $ \begin{array}{r} \hline e & y \\ 1.00 & 0.10 \\ 0.80 & 0.10 \\ 0.65 & 0.10 \\ 0.65 & 0.10 \\ 0.50 & 0.08 \\ 0.40 & 0.08 \\ 0.30 & 0.05 \\ \end{array} $	_	
Positional tolerance of terminal tips	t	(1) The tolerance of the terminal tips shall be specified in the outline drawing. $\begin{array}{c c c c c c c c c c c c c c c c c c c $	_	
Number of terminal	n	The number of terminals are determined as follows. (1) Group1 (See <b>Table 2</b> ) $\cdot \boxed{D} \leq 14.00$ The number of terminals obtained when $Z_D \geq 0.10 + b_{1nom}/2$ $\cdot \boxed{D} > 14.00$ The number of terminals obtained when $Z_D \geq 0.20 + b_{1nom}/2$ (2) Group2,3 (See <b>Table 3,4</b> ) $\cdot \boxed{e} = 1.00, 0.80, 0.65, 0.50$ The number of terminals obtained when $Z_D \geq 0.50$ $\cdot \boxed{e} = 0.40, 0.30$ The number of terminals obtained when $Z_D \geq 0.20 + b_{1nom}/2$	The recommended number of terminals in a package length for each terminal pitch is the maximum number that shall meet the rule given in the standards column, except number of terminals in Group1, $D=12.50$ , $D=14.00$ of $e=0.40$ . (See in Table 2 and note)	the same terminal pitch. And it must not overlap in different

## 8.2 Group 2

## Table1 (continued)

Description	Reference symbol	Standards	Recommended	Remarks
Width between first bent part of terminal	Gıe	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	_	Nominal dimension for tes socket and tray design.
Package overhang	Z <sub>D</sub>	Z <sub>D</sub> =( D -(n/2-1)×e)/2		
Length of flat part of terminal Terminal length	L L1	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	_	Value of L and L <sub>1</sub> must be selected in the same Group (1, 2, 3) of E/D.
Terminal inline interval	ee	$eE = H_{e}$ -Lpnom (1) In case of SOP and SSOP and TSOP(2), $eE = e_{1}$ (2) In case of TSOP(1) , $eD = e_{1}$	_	

#### 8.3 Number of terminal list

#### Table 3 Number of terminal list (Group 1)

е	1.(	00	0.8	30	0.0	65	0.	50	0.4	0	0.3	30
D	n	ZD	n	Z <sub>D</sub>	n	Z <sub>D</sub>	n	Z <sub>D</sub>	n	Z <sub>D</sub>	n	ZD
2.00							* 8	0.25	* 10	0.20	* 12	0.25
3.00					* 8	0.525	10	0.50	* 14	0.30	16	0.45
3.60			* 8	0.60	* 10	0.50	* 12 * 14	0.25	16	0.40	* 18 20	0.30
		4.00	* 10				10	0.50	* 18	0.20	* 22	0.30
5.00	8 * 10	1.00 0.50	* 12	0.50	14 * 16	0.55 0.225	18 * 20	0.50 0.25	22 * 24	0.50 0.33	26 28	0.70 0.55
											30 * 32	0.40 0.25
6.50	* 12	0.75		0.55	18	0.65	22	0.75	28	0.65	34	0.85
			* 16	0.225	* 20	0.325	24 * 26	0.50 0.25	30 * 32	0.45 0.25	36 38	0.70 0.55
							20	0.25	52	0.23	40	0.40
7.80	14	0.90	18	0.65	22	0.65	28	0.65	34	0.70	* 42 44	0.25
7.00	* 16	0.40	* 20	0.325	* 24	0.325	* 30	0.40	36	0.50	46	0.60
									* 38	0.30	48 * 50	0.45 0.30
9.70	18	0.85	22	0.65	26	0.95	32	1.10	40	1.05	52	1.10
	* 20	0.35	* 24	0.325	28 * 30	0.625 0.30	34 36	0.85 0.60	42 44	0.85 0.65	54 56	0.95 0.80
					50	0.00	* 38	0.35	46 * 48	0.45	58	0.65
									* 48	0.25	60 62	0.50 0.35
											* 64	0.20
11.00	* 22	0.50	26 * 28	0.70 0.30	32 * 34	0.625 0.30	40 42	0.75 0.50	50 52	0.70 0.50	66 68	0.70 0.55
							* 44	0.25	* 54	0.30	70	0.40
12.50	* 24	0.75	30	0.65	36	0.725	46	0.75	56	0.85	* 72 74	0.25
			* 32	0.25	* 38	0.40	48	0.50	58 * 60	0.65	76	0.70
							* 50	0.25	<sup></sup> 60	0.45	78 80	0.55 0.40
11.00	00	1.00	* 34	0.00	40	0.005	50	0.75	60	4.00	* 82	0.25
14.00	26 * 28	1.00 0.50	- 34	0.60	40 * 42	0.825 0.50	52 54	0.75 0.50	62 64	1.00 0.80	84 86	0.70
							* 56	0.25	66 * 68	0.60 0.40	88 90	0.55 0.40
											* 92	0.20
(15.80)	28 30	1.40 0.90	36 * 38	1.10 0.70	44 46	1.075 0.75	58 60	0.90 0.65	70 72	1.10 0.90	94 96	1.00 0.85
	* 32	0.90		0.70	* 48	0.425	* 62	0.00	74	0.70	98	0.70
									76 * 78	0.50 0.30	100 *102	0.55 0.40
(17.00)	* 34	0.50	40	0.90	50	0.70	64	0.75	78	0.90	104	0.85
			* 42	0.50	* 52	0.375	* 66	0.50	80 82	0.70 0.50	106 108	0.70 0.55
									* 84	0.30	*110	0.40
(18.50)	* 36	0.75	44 * 46	0.85 0.45	54 * 56	0.80 0.475	68 70	1.00 0.75	84 86	1.05 0.85	112 114	1.00 0.85
			40	0.40	00	0.470	* 72	0.50	88	0.65	116	0.70
									* 90	0.45	118 *120	0.55 0.40
(20.80)	38	1.40		1.20	58	1.30	74		90	1.60	0	
	40 * 42	0.90 0.40		0.80 0.40	60 62	0.98 0.65	76 78	1.15 0.90	92 94	1.40 1.20		
					* 64	0.325	80	0.65	96	1.00		
							* 82	0.40	98 100	0.80 0.60		
									*102	0.40		

Note (1)  $D \le 14.00 : Z_D \ge 0.10 + b_{1nom}/2, D > 14.00 : Z_D \ge 0.20 + b_{1nom}/2$ (2) The recommended number of terminals in a package length is the maximum number in the package length, except package length D = 12.50, 14.00 for the terminal pitch of e = 0.40 mm. Because they are adjusted with JEDEC. (See "\*" in **Table 2**) But, it is different from the number of the maximum terminals which is found by Note (1).

### 8.3 Number of terminal list

#### Table 3 Number of terminal list (Group 2)

е	1.00 0.80 0.65							.50		.40	0.30		
D	n	ZD	0.	ZD	n	.05 Z <sub>D</sub>	n	.50 Z <sub>D</sub>	n	.40 Z <sub>D</sub>	n [	Z <sub>D</sub>	
10.79	* 20	0.895	22	1.395	28	1.17	34	1.395	44	1.195	54	1.495	
10.75	20	0.000	24	0.995	30	0.845	36	1.145	46	0.995	56	1.345	
			* 26	0.595	* 32	0.52	38	0.895	48	0.795	58	1.195	
			-				* 40	0.646	50	0.595	60	1.045	
									* 52	0.395	62	0.895	
											64	0.745	
											66	0.595	
											68	0.445	
											* 70	0.295	
12.06	22	1.03	* 28	0.83	34	0.83	42	1.03	54	0.83	72	0.78	
	* 24	0.53			* 36	0.505	44	0.78	56	0.63	74	0.63	
							* 46	0.53	* 58	0.43	76	0.48	
											* 78	0.33	
13.33	* 26	0.665	30	1.065	* 38	0.815	48	0.915	60	0.865	80	0.815	
			* 32	0.665			* 50	0.665	62	0.665	82	0.665	
									* 64	0.465	84	0.515	
11.00	* 00	0.00	0.4	0.00	40	4 405	50	4.05	00	0.00	* 86	0.365	
14.60	* 28	0.80	34 * 36	0.90 0.50	40 * 42	1.125 0.8	52	1.05	66 68	0.90 0.70	88	0.85 0.70	
			30	0.50	42	0.0	54 * 56	0.80 0.55	68 70	0.70	90 92	0.70	
							50	0.55	* 72	0.30	* 94	0.55	
15.87	* 30	0.935	* 38	0.735	44	1.11	58	0.935	74	0.30	94	0.40	
15.67	30	0.935	30	0.735	* 46	0.785	* 60	0.935	76	0.735	90 98	0.885	
					40	0.705	00	0.005	* 78	0.335	100	0.585	
									10	0.000	100	0.435	
											*104	0.285	
17.14	32	1.07	40	0.97	48	1.095	62	1.07	80	0.77	106	0.77	
	* 34	0.57	* 42	0.57	* 50	0.77	64	0.82	82	0.57	108	0.62	
	0.	0.01		0.01		•	* 66	0.57	* 84	0.37	110	0.47	
									_		*112	0.32	
18.41	* 36	0.705	* 44	0.805	52	1.08	68	0.955	86	0.805	114	0.805	
					* 54	0.755	* 70	0.705	88	0.605	116	0.655	
									* 90	0.405	118	0.505	
											*120	0.355	
19.68	* 38	0.84	46	1.04	56	1.065	72	1.09	92	0.84			
			* 48	0.64	* 58	0.74	74	0.84	94	0.64			
	* 40	0.075	* 50	0.075		4.05	* 76	0.59	* 96	0.44			
20.95	* 40	0.975	* 50	0.875	60	1.05	78	0.975	98	0.875			
					* 62	0.725	* 80	0.725	100	0.675 0.475			
00.00	40	4 4 4	50	4 4 4	04	4 005	00	4 4 4	*102	00			
22.22	42 * 44	1.11 0.61	52 * 54	1.11 0.71	64 * 66	1.035 0.71	82 84	1.11 0.86	104 106	0.91 0.71			
	44	0.01	54	0.71	00	0.71	°4 * 86	0.60	*108	0.71			
							00	0.01	110	0.31			
23.49	* 46	0.745	56	0.945	68	1.02	88	0.995	112	0.745			
23.49	40	0.745	* 58	0.545	* 70	0.695	* 90	0.995	114	0.745			
			00	0.040	10	0.000	50	0.7 40	116	0.345			
24.76	* 48	0.88	* 60	0.78	72	1.005	92	1.13	118	0.78			
2 1.7 0	10	0.00	00	0.70	* 74	0.68	94	0.88	120	0.58			
l					, ,	0.00	* 96	0.63	.20	0.00			
26.03	50	1.015	62	1.015	76	0.99	98	1.015				;	
_ 5.00	* 52	0.515	* 64	0.615	* 78	0.665	100	0.765					
l	-		-		-		*102	0.515					
27.30	* 54	0.65	* 66	0.85	80	0.975	104	0.90					
l				-	* 82	0.65	*106	0.65					
28.57	* 56	0.785	68	1.085	84	0.96	108	1.035					
	1		* 70	0.685	* 86	0.635	*110	0.785					

Note (1)  $e = 1.00, 0.80, 0.65, 0.50 : Z_D \ge 0.50, e = 0.40, 0.30 : Z_D \ge 0.20 + b_{1nom}/2$ (2) The recommended number of terminals in a package length is the maximum number in the package length (D). (See "\*" in Table 3)

#### 8.3 Number of terminal list

```
Table 4 Number of terminal list (Group 3)
```

е	1.	.00	0.8	80	0	.65	0	.50	0	.40	0.3	30
D	n	ZD	n	ZD	n	ZD	n	ZD	n	ZD	n	ZD
13.25	* 24	1.125	26	1.825	32	1.75	40	1.875	52	1.625	66	1.825
		-	28	1.425	34	1.425	42	1.625	54	1.425	68	1.675
			* 30	1.025	36	1.10	44	1.375	56	1.225	70	1.525
					* 38	0.775	46	1.125	58	1.025	72	1.375
							48	0.875	60	0.825	74	1.225
							* 50	0.625	62	0.625	76	1.075
									* 64	0.425	78	0.925
											80	0.775
											82	0.625
											84	0.475
											* 86	0.325
14.50	* 26	1.25	32	1.25	40	1.075	52	1.00	66	0.85	88	0.80
			* 34	0.85	* 42	0.75	54	0.75	68	0.65	90	0.65
							* 56	0.50	* 70	0.45	92	0.50
											* 94	0.35
15.75	28	1.375	36	1.075	44	1.05	58	0.875	72	0.875	96	0.825
	* 30	0.875	* 38	0.675	* 46	0.725	* 60	0.625	74	0.675	98	0.675
									* 76	0.475	100	0.525
											*102	0.375
17.00	* 32	1.00	* 40	0.90	48	1.025	62	1.00	78	0.90	104	0.85
					* 50	0.70	64	0.75	80	0.70	106	0.70
							* 66	0.50	82	0.50	108	0.55
									* 84	0.30	*110	0.40
18.25	* 34	1.125	42	1.125	52	1.00	68	0.875	86	0.725	112	0.875
			* 44	0.725	* 54	0.675	* 70	0.625	88	0.525	114	0.725
									* 90	0.325	116	0.575
											118	0.425
											*120	0.275
19.50	* 36	1.25	* 46	0.95	56	0.975	72	1.00	92	0.75		
					* 58	0.65	74	0.75	94	0.55		
							* 76	0.50	* 96	0.35		
20.75	38	1.375	48	1.175	60	0.95	78	0.875	98	0.775		
	* 40	0.875	* 50	0.775	* 62	0.625	* 80	0.625	100	0.575		
									*102	0.375		
22.00	* 42	1.00	* 52	1.00	64	0.925	82	1.00	104	0.80		
					* 66	0.60	84	0.75	106	0.60		
							* 86	0.50	*108	0.40		
23.25	* 44	1.125	54	1.225	68	0.90	88	0.875	110	0.825		
			* 56	0.825	* 70	0.575	* 90	0.625	112	0.625		
									*114	0.425		
24.50	* 46	1.25	56	1.45	72	0.875	92	1.00	116	0.85		
			58	1.05	* 74	0.55	94	0.75	118	0.65		
	10	1.075	* 60	0.65		0.05	* 96	0.50	*120	0.45		
25.75	48	1.375	* 62	0.875	76	0.85	98	0.875				
07.00	* 50	0.875			* 78	0.525	*100	0.625				
27.00	* 52	1.00	64	1.10	80	0.825	102	1.00				
			* 66	0.70	* 82	0.50	104	0.75				
00.05	* ~ 4	4 405	* ~~~	0.005	* 0.4	0.00	*106	0.50				
28.25	* 54	1.125	* 68	0.925	* 84	0.80	108	0.875				
00.50	* = 0	4.05	70	A 4 F	00	4 4 0	*110	0.625				
29.50	* 56	1.25	70 * 70	1.15	86	1.10	112	1.00				
			* 72	0.75	* 88	0.775	114	0.75				
20.75		1 075	70	4 075	00	1 075	*116	0.50	1			
30.75	58 * 60	1.375	72 * 74	1.375	90 * 02	1.075	118	0.875				
	* 60	0.875	* 74	0.975	* 92	0.75	*120	0.625				

 $\begin{array}{ll} \textbf{Note(}^1) & \textcircled{e} = 1.00, \, 0.80, \, 0.65, \, 0.50: \ensuremath{Z_D} \geq 0.50, \ensuremath{\textcircled{e}} = 0.40, \, 0.30: \ensuremath{Z_D} \geq 0.20 + b_{1nom}/2 \\ \textbf{(}^2) & \textbf{The recommended number of terminals in a package length is the maximum number in the package length (D). (See "*" in Table 4) \\ \end{array}$ 

#### 8.4 Standard Registration

When you need to register a new outline specification on the standard, complete the appendix format 5 in Technical Standardization Committee on Semiconductor Device Package steering rule, in compliance with the Standardization Rule. In order to make a package dimension table, which come sunder Item 2, Appendix format 5, fill the dimensions marked with  $(\nu)$  in the following Table.

Incidentally, it supposes that it enters package code form type according to **EIAJ ED-7303A** (Name and Code for Integrated Circuits Package).

Serial I	Number	a set in an		
Extern	al Type	P-OSSOP( (P-OS	000-0000 × 000 sop000-0000-c	)0-0.00 ).00)
Reference Symbol		min	nom	max
	D		レ	
	E		レ	
	A <sub>2</sub>		<b>ل</b>	V
	A			
	A <sub>1</sub>	V	<u>ل</u>	V
	A <sub>3</sub>		V	
	b <sub>p</sub>	V		V
	b <sub>1</sub>	V	レ レ	レ
Group 1	С			レ
lno,	C <sub>1</sub>		V	レ
ซิ	θ			
	HE			
	Lp		<u>ل</u>	
	е		レ(*)	
	x	· · · · · · · · · · · · · · · · · · ·		
	у			レ
	t			
	f			
	n		V	
	Z <sub>D</sub>			
Group 2	L			
Lou				· ·
G	eE			
	G <sub>1E</sub>			

#### Table 5

\*: Means true geometrical position

## 9. STANDARD PACKAGE LIST

To further clarify the combinations of part dimensions, the combinations of recommended package classifications shall be indicated as shown below as assistance in the design and development of new packages in the future.

## 9.1 Relation of Package width $\mathbf{E}$ , Body height $A_{2nom}$

Pac	ckage wid	thE			Во	dy height A	2nom				
Group 1	Group 2	Group 3	2.70	1.70	1.50	1.40	1.20	1.00	0.80	0.60	Remark
3.00						]					L/L1= 0.30/0.50
4.40				*\$\$	:OP		*SSOP	*TSSOP	*VSSOP	*WSSOP	
5.60								10001	V330F	WOOUF	
6.10											
	7.62					*LSSOP	]	TSOP2	VSOP2		L/L1= 0.50/1.00
(8.00)				*SS	OP		*SSOP	*TSSOP	*VSSOP	*WSSOP	
	8.89					*LSSOP		TSOP2	VSOP2		L/L1=0.50/1.00
(9.90)				*\$\$	OP		*SSOP	*TSSOP	*VSSOP	*WSSOP	
	10.16							TSOP2	VSOP2		L/L1= 0.50/1.00
		11.50									
		13.00	**SSOP			*LSSOP		TSOP2 L/L1=	V SOP2 L/L1=		
		14.50				20001		0.50/1.00	0.50/1.00		
		16.00		·							

P-SSOP (term inal pitch e = 1.00 - 0.30)

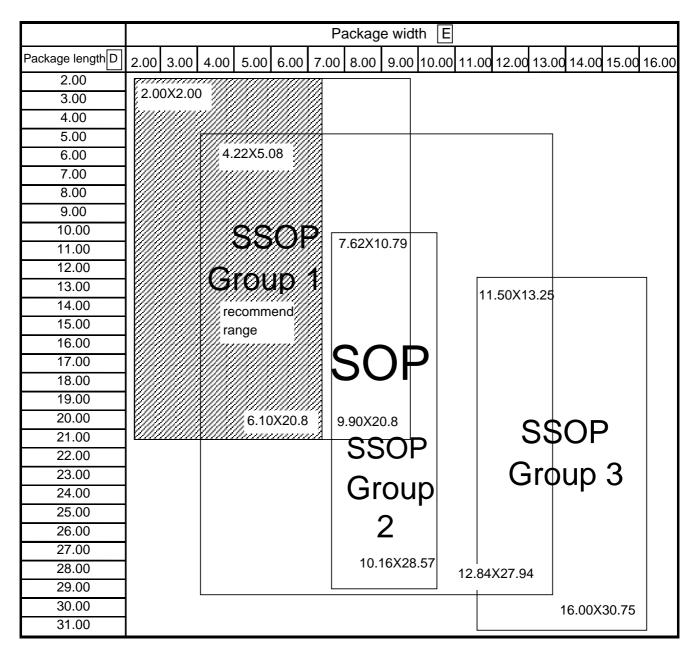
\*:L/L1=0.50/1.00 \*\*:L/L1=0.80/1.60

	Seated	
	height	
	Amax	Remark
LSSOP	1.70	
TSSOP	1.20	(TSOP2)
VSSOP	1.00	(VSOP2)
WSSOP	0.80	(WSOP2)

P-SOP(terminal pitch e=1.27)

		Package						
Nom inal d	Nominal dimensions width		Body height A <sub>2nom</sub>					
e	E	Emax	2.70	1.70	1.50	1.40	1.20	Remark
225m il	5.72	4.72						L/L1=0.50/1.00
300m il	7.62	6.62						
375m il	9.53	8.13	COD					
450m il	11.43	10.03	SOP				L/L1=0.80/1.60	
525m il	13.24	11.94						
600m il	15.24	13.84						

## 9.2 Relation of SOP, SSOP with Package length D, Package width E



## **Explanatory notes**

#### 1. PURPOSE

This technical report is intended to provide the industrial standards for shrink small outline packages(to be referred to as P-SSOP hereinafter) and the design guidelines in producing the P-SSOP and developing the automatic mounting machines and related parts.

#### 2. PROGRESSES OF DELIBERATION

Along with a recent trend toward higher integration and higher function devices, larger dies and increase pin numbers have been requested, devices have been made smaller and lighter, and consequently, miniaturization of semiconductor packages is now indispensable. In order to meet these requests, the general rules for P-SSOP outlines, **EIAJ ED-7402-2**, were enacted and issued in February 1990 through deliberation by the Technical Committee on Semiconductor Package Outline (predecessor of Technical Standardization Committee on Semiconductor Device Package), apart from the conventional small outline packages (to be referred to as P-SOP hereinafter). After that, we examined the actually produced packages and found many inconsistencies with the general rules. Therefore, the P-SSOP General Rules Preparation Working Group in the Plastic Package Sub committee (predecessor of IC Package Sub Committee) revised them preponderantly since March 1992, and the revised rules, **EIAJ ED-7402-2A**, were enacted and issued in November 1993 through deliberation by the Special Technical Committee on Semiconductor Device Packages(predecessor of Technical Committee on Semiconductor Device Packages).

**EIAJ ED-7402-2A** was revised that an emphasis was placed on linking the package sizes to TSOP(2), main force of memory packages, as to large P-SSOP for memory which have been expected as future package line-up. This resulted in the general rules with two dimensional series; one is hard metric series (package width of less than 300 mil) considering actual package sizes based on the conventional **EIAJ ED-7402-2**, and soft metric series(package width of 300 mil or more) taking the thick TSOP(2) type as its concept.

Furthermore, in order to introduce postponed datum and coordinate with the JEDEC (Joint Electron Device Engineering Council) after enactment of the **EIAJ ED-7402-2A**, the Plastic Package Subcommittee(predecessor of IC Package Sub Committee) and P-SSOP Project Group started revision work since July 1995, putting emphases on review of proposed styles and clarification of location as design guide, considering standardization of the IEC(International Electro technical Commission) aiming at international standardization, as well as review of maximum number of terminals in each package length for the hard metric series and inclusion of 400 mil or more dimensions in the hard metric series for the soft metric series. And **EIAJ EDR-7314** was established in August 1996.

This technical report, **EIAJ EDR-7314A** is revised ,the revision which is in the package outline drawing form to have conformed to the IEC standard after considering a consistence with **EIAJ ED-7311-20**[Standard of integrated circuits package (P-SSOP)] which was established in December 2001 in IC Package Sub Committee.

Furthermore, it added new Seated height cord "V"(VSSOP(Very thin SSOP):Seated height  $A_{max}$ =1.00mm),and "W" (WSSOP(Very Very thin SSOP):Seated height  $A_{max}$ =0.80mm). Also, it added an equal to or less than 3.00 mm area, package width([E]), the package length ([D]) which to think that is increased in by the production in the future. This technical report, **EIAJ EDR-7314A** is established in January 2002 in IC Package Sub Committee.

#### 3. BASIC IDEA

#### (1) Datum

The datum and geometrical tolerances were adopted this technical report. In adopting the datum, we referred to the JEDEC standards for various packages, and registered packages. For the datum in package length, a point 0.10mm inside the terminal nose at the package center was set as datum target, sharing a tolerance for the terminal center position (X).

#### (2) Outer coating of the terminals

The terminal width and thickness, limited to solder plating. This technical report provides them for extremely thin plating such as palladium.

#### (3) Package outline drawing and Allowable range of soldering part of terminals

According to the IEC standard, As the rule with the terminal width ( $b_p$ ) of Package outline drawing, it added to tolerance for terminal center position (X) and it gained means projected tolerance zone (P). It's meaning shows the range of the measurement object and guaranteeing the height of  $\overline{A_3} = 0.25$ . Also, it changes the symbol of Allowable range of soldering part of terminals into Terminal interval pitch( $\overline{eE}$ ) from Overall width( $\overline{H_E}$ ) and relation is  $\overline{eE} = \overline{H_E} - L_{pnom}$ .

#### 4. BACKGROUND FOR DIMENSIONAL PROVISIONS

#### (1) Nominal dimensions

Package width X Package length(Symbol :  $\begin{bmatrix} E \\ X \\ D \end{bmatrix}$ ) is applied to Nominal dimensions. However as the exception, old nominal dimensions is possible to use places (side by side) and it writes which package width (Symbol : E).

## (2) Package width ( E )

There are two hard metric series of package widths and lengths, and one soft metric series. Each series was grouped and its combinations were limited within the same group. One of large size hard metric series is 400mil or more dimensions in the soft metric series in the old **EIAJ ED-7402-2A** newly changed to the hard metric series, as same as SOJ, TSOP(2).Although resin burr between the terminals does not directly affect an outer dimension, it must be 0.25mm at maximum on single side from a viewpoint of unification of specification with the JEDEC.

ED-7404-2 1990.02		<b>ED-7404-2A</b> 1993.11	<b>N</b>		EDR-731 1996.08	4			EDR-7314/ 2002.01	A	
Enom	]	Enom			E				E		
		Hard Metric	Soft Metric	]	Group1	Group2	Group3	1	Group1	Group2	Group3
									2.00		
_									3.00		
4.20		4.40			4.40				4.40		
		5.60			5.60				5.60		
6.10	∣⇒	6.10		∣⇒	6.10				6.10		
8.00		8.00	7.62		(8.00)	7.62		⇒	(8.00)	7.62	
9.90		9.90	8.89		(9.90)	8.89			(9.90)	8.89	
			10.16			10.16				10.16	
11.80			11.43				11.50				11.50
			12.70				13.00				13.00
13.70			13.97				14.50				14.50
			15.24				16.00		:		16.00
		Coordin ation with existing products	Inclusion in same system as TSOP(2)				Inclusion of 400mil or more in Hard Metric systems		It adds equal to or less than E=3.00 ranges.	<b>7</b>	

#### (3) Progresses of revisions Package width (Enom/ E))

Note Parenthesized Package widths are not recommended

## (4) Package length( D )

At **EIAJ EDR-7314**, along with new inclusion of nominal dimensions and package widths in the hard metric series, a new package length series was added for 400 mil or wider packages. Since the resin burr or remainder of gate at the end face directly affects an outer dimension, they must be 0.15mm at maximum on single side (same specification as the JEDEC).

**EIAJ EDR-7314** is added an equal to or less than 3.00 mm area, the package length (D) which to think that is increased in by the production in the future.

#### (5) Terminal width and thickness $(b_p, b_1, c, c_1)$

At **EIAJ EDR-7314**, In line with the QFP design guide, the minimum, normal, and maximum values for,  $\boxed{e} = 0.30$  mm have been un-parenthesized. Also, the following ideas have been applied in order to deal with extremely thin plating (palladium) other than solder plating, for which a demand is expected to increase in the future.

Minimum value: A lower value for extremely thin plating has been adopted in orders to share the standards for solder plating and extremely thin plating.

$$b_{pmin} = b_{1min}$$
 ,  $c_{min} = c_{1min}$ 

Normal value: Divide because rough thickness values of solder plating and extremely thin plating differs from each other.

Solder plating:  $b_{pnom} = b_{1nom} + 0.02$  (Plating thickness rough side 0.01)

 $C_{nom} = C_{1nom} + 0.02$  (Plating thickness rough side 0.01)

Extremely thin plating:  $b_{pnom} = b_{1nom}$ ,  $c_{nom} = c_{1nom}$ 

Maximum value: A higher value for solder plating has been adopted in order to share the standards for solder plating and extremely thin plating.

e =0.40,0.30mm : Same values as QFP

Particularly, in case of e = 0.30mm for the terminal width (b<sub>p</sub>), and c<sub>nom</sub> = 0.15 or 0.10 for the terminal thickness (c), the values calculated with square root of sum of squares of material tolerance and plating tolerance was raised to 2-digit values below decimal point, considering the respective tolerances independent (in order to include the recommended values in **EIAJ EDR-7314**).

# (6) Allowable value for the terminal center position (x) and co planarity of the terminal bottom face (y)

In order to coordinate as same SOP (Small Outline Package) type packages,

- e = 1.00, 0.80, 0.65mm : the regular value have been adjusted to TSOP(2)
- e = 0.50mm : the regular value has been coordinated with the JEDEC standard
- e = 0.40, 0.30mm : the regular value have been coordinated QFP with gull wind lead

#### (7) Number of terminals(n)

This design guide has divided package lengths (  $\Box$  ) into three groups, 1 through 3, according to the package width( E ), and provided the minimum package overhangs for the respective groups to calculate the maximum number of terminals(= recommended number of terminals) in each package length. For Group1, part of the maximum numbers of terminals (= recommended numbers of terminals) differs from the maximum number of terminals calculated as described above, because it have been coordinated with the JEDEC standard.

In this design guide, the regular maximum number of terminals has been set to 120 obtained out of  $\boxed{e} = 0.50$ mm at maximum package length  $\boxed{D} = 30.75$ mm. This has been determined in order to maximally make use of the regular number of terminals at terminal linear spacing  $\boxed{e} = 0.50$ mm, which is the current mainstream, on the assumption that the SOP type package will not greatly exceed 100 pins for the moment.

#### (8) Package overhang(Z<sub>D</sub>)

#### (a) Group1

This group, equivalent to a small package group, wants to take as many terminals as it can. Particularly in case of  $\boxed{D} \le 14.00$ mm, the distance from the terminal end at the package corner to the package end has been set to 0.10mm which is a currently possible physical limit value, and limited by;

$$Z_D \ge 0.10 + b_{1nom}/2$$

In case of D > 14.00mm, the distance from the terminal end at the package corner to the package end has been set to 0.20mm, considering chipping of the package, and limited by;

$$Z_D \geq 0.20 + b_{1nom}/2$$

## (b) Group2, 3

Since the package width and length are the same as SOJ and TSOP(2), the number of terminals has been also limited in the same manner as in case of SOJ and TSOP(2), as far as e = 1.00, 0.80, 0.65 or 0.50mm. ( $Z_D \ge 0.50$ mm) For the terminal linear spacing e = 0.40 or 0.30mm, which has not been provided in SOJ or TSOP(2), the same limitations as those in case of Group1, D > 14.00mm have been applied.

(9) Terminal flat section length, terminal length, and terminal soldering section length (L, L<sub>1</sub>, L<sub>p</sub>) The old EIAJ ED-7402-2A uniformly provided "L/L<sub>1</sub> = 0.50/1.00" regardless of the package size, but later, installation strength was judged insufficient with the packages in the soft metric series(large package group). Therefore, only in case of the body height of A<sub>2</sub>nom = 2.70mm where reduced installation strength was particularly pointed out at "L/L<sub>1</sub> =0.50/1.00," from old EIAJ EDR-7314 has adopted "L/L<sub>1</sub> = 0.80/1.60" which has a good track record of installation strength.

And this design guide **EIAJ EDR-7314A** is adopted "L/L<sub>1</sub> = 0.30/0.50" in the case below package width  $\boxed{E}$ =3.00mm, it corresponded to made smaller of a package.

Due to new introduction of soldering section length of Lp in the old **EIAJ ED-7402-2A** in line with the IEC format, the conventional terminal flat section length L has been changed from the foreign assurance value (dimension with tolerance) to the dimension used only for package design (dimension without tolerance), and so has the terminal length  $L_1$  due to new introduction of the datum and geometrical tolerance in this design guide.

#### (10) Terminal first bent spacing (G<sub>1E</sub>)

From old **EIAJ EDR-7314**, As with QFP, it was added nominal values of terminal first bent spacing for reference of test sockets and tray design.

#### 5. PACKAGE NAMES

According to the package height code which was established in the **EIAJ ED-7303A** and IEC format, thin body type SSOP made the name which is with low seated height, It inewly added VSSOP (Very thin SSOP), WSSOP (Very Very thin SSOP) in addition to LSSOP (Low profile SSOP), TSSOP (Thin SSOP) from this design guide. Seated height (A) and ( $A_{2nom}$ ) are shown below.

$$\begin{split} & \text{LSSOP}(\text{Low profile SSOP}): \text{seated height } A_{\text{max}} = 1.70 \text{mm}, \ A_{\text{min}} > 1.20 \text{mm}, \ \text{body height } A_{2\text{nom}} = 1.40 \text{mm} \\ & \text{TSSOP}(\text{Thin SSOP}) : \text{seated height } A_{\text{max}} = 1.20 \text{mm}, \ A_{\text{min}} > 1.00 \text{mm}, \ \text{body height } A_{2\text{nom}} = 1.00 \text{mm} \\ & \text{VSSOP}(\text{Very thin SSOP}) : \text{seated height } A_{\text{max}} = 1.00 \text{mm}, \ A_{\text{min}} > 0.80 \text{mm}, \ \text{body height } A_{2\text{nom}} = 0.80 \text{mm} \\ & \text{WSSOP}(\text{Very Very thin SSOP}) : \text{seated height } A_{\text{max}} = 0.80 \text{mm}, \ A_{\text{min}} > 0.65 \text{mm}, \\ & \text{body height } A_{2\text{nom}} = 0.60 \text{mm} \end{split}$$

#### **COMMITTEE MEMBERS**

The IC Package Subcommittee of the Technical Standardization Committee on Semiconductor Device Packages has mainly deliberated this standard.

The subcommittee members are shown below.

<technical standardiz<="" th=""><th>zation Committee on Semiconductor Device Package&gt;</th><th></th></technical>	zation Committee on Semiconductor Device Package>							
Chairman	Chairman ELPIDA MEMORY, INC.							
< IC plastic Package Subcommittee>								
Chief	SANYO ELECTRIC CORP.	Hideyuki Iwamura						
Co- chief	SHARP CORP.	Katuyuki Tarui						
	FUJITSU LTD.	Hiroshi Inoue						
	TOSHIBA CORP.	Yasuhiro Koshio						
	MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.	Toshiyuki Fukuda						
Members	AMKOR THECHNOROGY JAPAN. INC.	Naomitchi Syoji						
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	SUMITOMO 3M CORP.	Akiko Tsubota						
	SEIKO EPSON CORP.	Yoshiaki Emoto						
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	IBM JAPAN CORP.	Tuneo Kobayashi						
	TEXAS INSTRUMENTS JAPAN LTD.	Takayuki Ohuchida						
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	FUJI ELECTRIC CO., LTD.	Osamu Hirohashi						
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