



日本電子機械工業会技術レポート

Technical Report of Electronic Industries Association of Japan

***EIAJ EDR-7321***

(HIE D-7409改正)

**集積回路パッケージデザインガイド  
クワッドフラット I-リードパッケージ**

**Design guideline of integrated circuits  
for Quad Flat I-lead package  
(QFI)**

1999年2月制定

作 成

**半導体パッケージ標準化委員会**

Technical Standardization committee on Semiconductor Device Package

発 行

**社団法人 日本電子機械工業会**

Electronic Industries Association of Japan

## **Design guideline of integrated circuits for Quad Flat I-lead package (QFI)**

### **1. SCOPE OF APPLICATION**

This standard regulated outline drawings and dimensions of surface quad flat I-lead package (QFI), especially plastic packages, classified as form A under the **EIAJ ED-7300** (standard for preparing standard outline drawings (integrated circuits) of semiconductor devices).

### **2. TERMS**

The definition of the terms used in this technical report complies with the **EIAJ ED-7300**. New terms will defined in the description of this technical report.

### **3. BACKGROUND**

This technical report is intended to standardize the outer dimensions of QFIs and ensure compatibility between products. This standard shows the standard design values on the concept of the design centers as far as possible for standardization.

### **4. DEFINITION OF PACKAGE**

A QFI is classified as Form A with I Terminal in the **EIAJ ED-7401A**, and defined a package with formed terminals which are led out of itself in four directions and are vertical toward the outside in junctions outside the package body for mounting on PCB surface.

### **5. NUMBER OF TERMINALS**

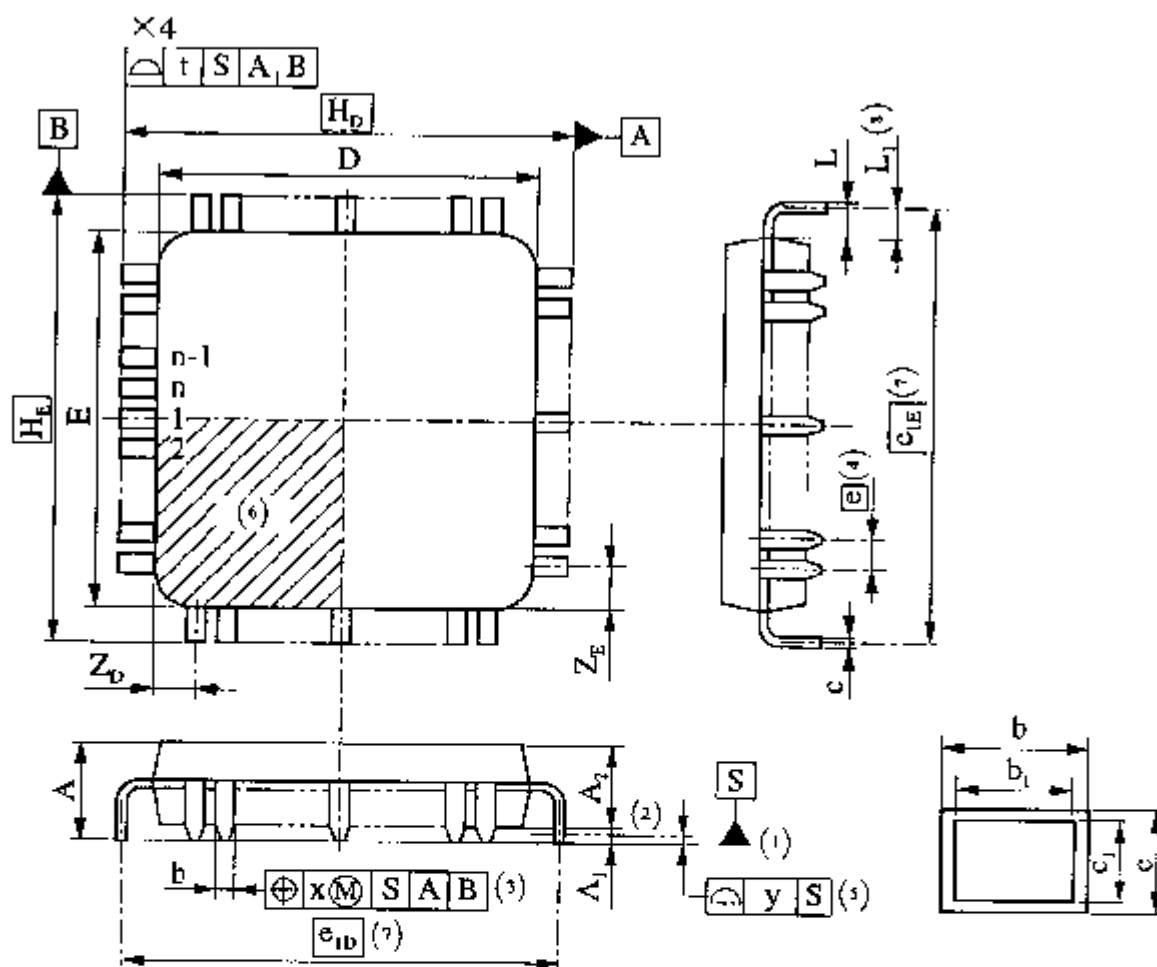
Numbering of Terminals complies with the **EIAJ ED-7300**.

### **6. NOMINAL DIMENSIONS**

The dimensions of center of outer-leads line (Symbol:  $e_{12} \times e_{11}$ ) is applied to nominal dimensions.

## 7. REFERENCE CHARACTERS AND DRAWINGS

QFI Outline drawings

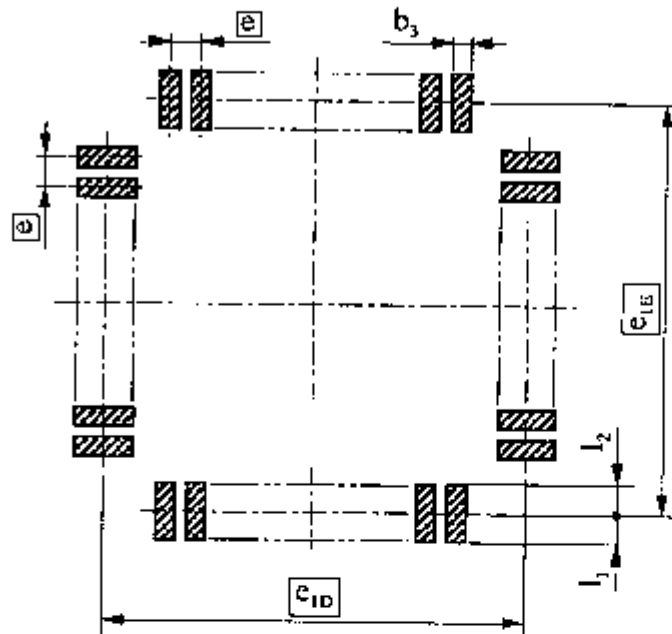


### Notes:

- (1) The mounting surface, with which a package is in contact.
- (2) The base surface, which is in parallel with the mounting surface and links the lowest points, except the stand-off.
- (3) The positional tolerances are applied to all terminals.
- (4) Specifies the true geometric position of terminal axis.
- (5) Specifies the vertical shift of flat part of each terminal from the mounting surface.
- (6) Shows the allowable position of index mark, which must be included in the shaded area entirely.
- (7) The dimensions of center of outer-leads line (Symbol:  $e_{LD} \times e_{LD}$ ) is applied to nominal dimensions.
- (8) This dimension is center of leads from package side

**REMARKS. ALLOWABLE RANGE OF SOLDERING PART OF TERMINALS.**

The range where the terminals to be soldered can exist is shown in this figure as reference for the foot print design.



$$L_{1max} = (t - c_{max}) / 2$$

$$L_{2max} = (t - c_{max}) / 2$$

$$b_{3max} = b_{max} + x$$

## 8. Overall Dimensions, Recommended value, Supplement

## 8.1 Group1

Table 1

unit: mm

Name	Reference symbol	Standards	Recommended value	Remarks																																																
Nominal dimension	$e_{1D} \times e_{1E}$	<p>• The dimensions of center of outer-leads line (Symbol: <math>e_{1D} \times e_{1E}</math>) is applied to nominal dimensions.</p> <p>• Nominal dimension are defined below;</p> $e_{1D} = D + 2 \times L_1$ $e_{1E} = E + 2 \times L_1$ <p>Nominal dimensions are listed below.</p> <p>Square (<math>e_{1D} \times e_{1E}</math>)</p> <table><tr><th>Terminal Pitch Nominal dimension (mil) <math>e_{1D} \times e_{1E}</math></th><th colspan="3">Number of terminals</th></tr><tr><th></th><th>1.270 (50mil)</th><th>1.016 (40mil)</th><th>0.762 (30mil)</th></tr><tr><td>5.08 × 5.08 (200 × 200)</td><td>—</td><td>—</td><td>20</td></tr><tr><td>6.35 × 6.35 (250 × 250)</td><td>—</td><td>20</td><td>—</td></tr><tr><td>7.62 × 7.62 (300 × 300)</td><td>20</td><td>—</td><td>28</td></tr><tr><td>8.89 × 8.89 (350 × 350)</td><td>—</td><td>28</td><td>—</td></tr><tr><td>10.16 × 10.16 (400 × 400)</td><td>28</td><td>—</td><td>44</td></tr><tr><td>12.70 × 12.70 (500 × 500)</td><td>—</td><td>44</td><td>52</td></tr><tr><td>15.24 × 15.24 (600 × 600)</td><td>44</td><td>52</td><td>—</td></tr><tr><td>17.78 × 17.78 (700 × 700)</td><td>52</td><td>—</td><td>68</td></tr><tr><td>20.32 × 20.32 (800 × 800)</td><td>—</td><td>68</td><td>84</td></tr><tr><td>22.86 × 22.86 (900 × 900)</td><td>68</td><td>84</td><td>100</td></tr></table>	Terminal Pitch Nominal dimension (mil) $e_{1D} \times e_{1E}$	Number of terminals				1.270 (50mil)	1.016 (40mil)	0.762 (30mil)	5.08 × 5.08 (200 × 200)	—	—	20	6.35 × 6.35 (250 × 250)	—	20	—	7.62 × 7.62 (300 × 300)	20	—	28	8.89 × 8.89 (350 × 350)	—	28	—	10.16 × 10.16 (400 × 400)	28	—	44	12.70 × 12.70 (500 × 500)	—	44	52	15.24 × 15.24 (600 × 600)	44	52	—	17.78 × 17.78 (700 × 700)	52	—	68	20.32 × 20.32 (800 × 800)	—	68	84	22.86 × 22.86 (900 × 900)	68	84	100	$e_{1D} = e_{1E}$	
Terminal Pitch Nominal dimension (mil) $e_{1D} \times e_{1E}$	Number of terminals																																																			
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22.86 × 22.86 (900 × 900)	68	84	100																																																	
Package Width	E	<p>• Package width E, and Package length D are defined below.</p> $E = e \times \left( \frac{n}{4} - 1 \right) + 2 \times Z_L$ $D = e \times \left( \frac{n}{4} - 1 \right) - 2 \times Z_{0L}$		Dimension E, D does not include resin fines and gate remainders. Maximum and Minimum dimension defined Package Standards.																																																
Package length	D	Dimension E, D are not regulation, but desired including dimension ZE max, $Z_{0max}$ Stipulation.																																																		
Mounting height	A	Mounting height value is defined below: $A_{max} = 3.05$																																																		

Table 1 (continued)

unit: mm

Name	Reference symbol	Standards	Recommended value	Remarks																				
Stand-off height	$A_1$	Stand-off height value is defined below: $A_{1min} = 0$ $A_{1max} = 0.75$		It use properly within use.																				
Package height	$A_2$	Dimension E, D is not regulation, but desired including Recommended value dimension.	1.53 ~ 3.05	Maximum and Minimum dimension defined Package Standards.																				
Terminal pitch	$\boxed{e}$	$\boxed{e} = 1.270$ (50mil) $\boxed{e} = 1.016$ (40mil) $\boxed{e} = 0.762$ (30mil)		These values denote the true geometric position.																				
Terminal width	$b$	Maximum and Minimum dimension are below table to all terminal pitch. <table><tr><th><math>\boxed{e}</math></th><th><math>b_{min}</math></th><th><math>b_{max}</math></th></tr><tr><td>1.270 (50mil)</td><td>0.30</td><td>0.54</td></tr><tr><td>1.016 (40mil)</td><td>0.30</td><td>0.54</td></tr><tr><td>0.762 (30mil)</td><td>0.25</td><td>0.49</td></tr></table>	$\boxed{e}$	$b_{min}$	$b_{max}$	1.270 (50mil)	0.30	0.54	1.016 (40mil)	0.30	0.54	0.762 (30mil)	0.25	0.49	<table><tr><th><math>\boxed{e}</math></th><th><math>b_{nom}</math></th></tr><tr><td>1.270 (50mil)</td><td>0.42</td></tr><tr><td>1.016 (40mil)</td><td>0.42</td></tr><tr><td>0.762 (30mil)</td><td>0.37</td></tr></table>	$\boxed{e}$	$b_{nom}$	1.270 (50mil)	0.42	1.016 (40mil)	0.42	0.762 (30mil)	0.37	
	$\boxed{e}$	$b_{min}$	$b_{max}$																					
1.270 (50mil)	0.30	0.54																						
1.016 (40mil)	0.30	0.54																						
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$\boxed{e}$	$b_{nom}$																							
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1.016 (40mil)	0.42																							
0.762 (30mil)	0.37																							
	$b_1$	Maximum and Minimum dimension are below table to all terminal pitch. <table><tr><th><math>\boxed{e}</math></th><th><math>b_{min}</math></th><th><math>b_{max}</math></th></tr><tr><td>1.270 (50mil)</td><td>0.30</td><td>0.50</td></tr><tr><td>1.016 (40mil)</td><td>0.30</td><td>0.50</td></tr><tr><td>0.762 (30mil)</td><td>0.25</td><td>0.45</td></tr></table>	$\boxed{e}$	$b_{min}$	$b_{max}$	1.270 (50mil)	0.30	0.50	1.016 (40mil)	0.30	0.50	0.762 (30mil)	0.25	0.45	<table><tr><th><math>\boxed{e}</math></th><th><math>b_{nom}</math></th></tr><tr><td>1.270 (50mil)</td><td>0.37</td></tr><tr><td>1.016 (40mil)</td><td>0.37</td></tr><tr><td>0.762 (30mil)</td><td>0.32</td></tr></table>	$\boxed{e}$	$b_{nom}$	1.270 (50mil)	0.37	1.016 (40mil)	0.37	0.762 (30mil)	0.32	
$\boxed{e}$	$b_{min}$	$b_{max}$																						
1.270 (50mil)	0.30	0.50																						
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$\boxed{e}$	$b_{nom}$																							
1.270 (50mil)	0.37																							
1.016 (40mil)	0.37																							
0.762 (30mil)	0.32																							
Terminal thickness	$c$	$c_{min} = 0.17$ $c_{max} = 0.32$	$c_{nom} = 0.27$																					
	$c_1$	$c_{min} = 0.15$ $c_{1max} = 0.30$	$c_{1nom} = 0.25$																					

Table 1 (continued)

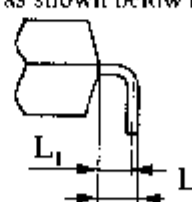
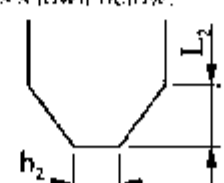
unit: mm

Name	Reference symbol	Standards	Recommended value	Remarks								
Tolerance of terminal center position	x	<p>(1) Tolerance of terminal center position express in outline drawings.</p> <div><div><div><div></div></div></div><div>x</div><div><div><div>M</div></div></div><div>S</div><div>A</div><div>B</div></div> <p>(2) The symbol x shall be replaced with any of the values shown below.</p> <table><tr><td><div>c</div></td><td>x</td></tr><tr><td>1.270 (50mil)</td><td>0.05</td></tr><tr><td>1.016 (40mil)</td><td>0.20</td></tr><tr><td>0.762 (30mil)</td><td>0.15</td></tr></table>	<div>c</div>	x	1.270 (50mil)	0.05	1.016 (40mil)	0.20	0.762 (30mil)	0.15		It applied MMC. ( $x \leq \frac{c}{5}$ )
<div>c</div>	x											
1.270 (50mil)	0.05											
1.016 (40mil)	0.20											
0.762 (30mil)	0.15											
Coplanarity	y	<div><div><div></div></div><div>y</div><div>S</div></div> <p>•The coplanarity shall be specified in the outline drwaing.</p> <p>•The symbol y shall be replaced with any of the values shown below.</p> <p>y = 0.15</p>										
Overall width	<div>H<sub>E</sub></div>	<p>•Nominal dimensions is defined below:</p> <p><div>H<sub>E</sub></div> = E + 2 × I.</p>										
Overall length	<div>H<sub>D</sub></div>	<p>•Nominal dimensions is defined below:</p> <p><div>H<sub>D</sub></div> = D + 2 × I.</p>										
Positional tolerance of terminal tips	t	<p>•The tolerance of the terminal tips shall be indicated in the outline drawing.</p> <div><div><div></div></div><div>:</div><div>S</div><div>A</div><div>B</div></div> <p>•The character t shall be replaced with any of values shown below.</p> <p>t = 0.35</p>										

## 8.2 Group2

Table 1 (continued)

unit: mm

Name	Reference symbol	Standards	Recommended value	Remarks																																															
Maximum number of terminal position	n	<p>Standard number of terminal shall be specified as shown below.</p> <table> <tr> <th rowspan="2">Terminal Pitch Nominal dimension (mil) <math>e_{10} \times e_{10}</math></th> <th colspan="3">Number of terminals</th> </tr> <tr> <th>1.270 (50mil)</th> <th>1.016 (40mil)</th> <th>0.762 (30mil)</th> </tr> <tr> <td>5.08 × 5.08 (200 × 200)</td> <td></td> <td>—</td> <td>20</td> </tr> <tr> <td>6.35 × 6.35 (250 × 250)</td> <td></td> <td>20</td> <td>—</td> </tr> <tr> <td>7.62 × 7.62 (300 × 300)</td> <td>20</td> <td>—</td> <td>28</td> </tr> <tr> <td>8.89 × 8.89 (350 × 350)</td> <td>—</td> <td>28</td> <td>—</td> </tr> <tr> <td>10.16 × 10.16 (400 × 400)</td> <td>28</td> <td>—</td> <td>44</td> </tr> <tr> <td>12.70 × 12.70 (500 × 500)</td> <td>—</td> <td>44</td> <td>52</td> </tr> <tr> <td>15.24 × 15.24 (600 × 600)</td> <td>44</td> <td>52</td> <td>—</td> </tr> <tr> <td>17.78 × 17.78 (700 × 700)</td> <td>52</td> <td>—</td> <td>68</td> </tr> <tr> <td>20.32 × 20.32 (800 × 800)</td> <td>—</td> <td>68</td> <td>84</td> </tr> <tr> <td>22.86 × 22.86 (900 × 900)</td> <td>68</td> <td>84</td> <td>100</td> </tr> </table>	Terminal Pitch Nominal dimension (mil) $e_{10} \times e_{10}$	Number of terminals			1.270 (50mil)	1.016 (40mil)	0.762 (30mil)	5.08 × 5.08 (200 × 200)		—	20	6.35 × 6.35 (250 × 250)		20	—	7.62 × 7.62 (300 × 300)	20	—	28	8.89 × 8.89 (350 × 350)	—	28	—	10.16 × 10.16 (400 × 400)	28	—	44	12.70 × 12.70 (500 × 500)	—	44	52	15.24 × 15.24 (600 × 600)	44	52	—	17.78 × 17.78 (700 × 700)	52	—	68	20.32 × 20.32 (800 × 800)	—	68	84	22.86 × 22.86 (900 × 900)	68	84	100		
Terminal Pitch Nominal dimension (mil) $e_{10} \times e_{10}$	Number of terminals																																																		
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Terminal length	L L <sub>1</sub>	<p>Terminal length defined as shown below (in outline drawing):</p> $L_1 = L - \left( \frac{e}{2} \right)$ <p><math>L_{min} = 0.5</math>  <math>L_{max} = 1.2</math></p> 	$L_{nom} = 0.75$																																																
Terminal Tip	b <sub>2</sub> L <sub>2</sub>	<p>Terminal Tip defined as shown below.</p> <p><math>L_{2nom} = 0.25</math>  <math>b_{2nom} = 0.20</math></p> 																																																	
Package over hang	Z <sub>+</sub> Z <sub>0</sub>	<p>Maximum values is defined below:</p> $Z_{0max}, Z_{0max} = 3 \times \boxed{e}$	$Z_{0max}, Z_{0max} = \boxed{e}$ or $1.5 \times \boxed{e}$ $\boxed{e}$ or $2 \times \boxed{e}$	$Z_{0max}, Z_{0max}$ is resin burrs or residual gates are not included.																																															



## 9. Standard Registration

When you need to register a new outline specification on the standard, complete the appendix format 5 in Technical Standardization Committee on Semiconductor Device Package steering rule, in compliance with the Standardization Rule.

In order to make a package dimension table, which comes under Item 2, Appendix format 5, fill the dimensions marked with ( ✓ ) in the following table 2.

Table 2

Serial Number				
External type		P-QFP-○○○○-○○○○×○○○○ ○. ○○		
Reference Symbol		min	nom	max
Group 1	$e_{LE}$		✓	
	$e_{LD}$		✓	
	E	✓	✓	✓
	D	✓	✓	✓
	$H_E$		✓	
	$H_D$		✓	
	A	✓		✓
	$A_1$	✓		✓
	$A_2$	✓	✓	✓
	b	✓	✓	✓
	$b_1$	✓	✓	✓
	c	✓		✓
	$c_1$	✓		✓
	$\bar{c}$		✓	
	x			✓
	y			✓
	t			✓
	n		✓	
Group 2	L		✓	
	$L_1$		✓	
	$L_2$		✓	
	$b_2$		✓	
	$Z_4$			✓
	$Z_0$			✓

## Design guideline of integrated circuits for Quad Flat I-Lead Package(QFI) Comments

### 1. Object

This technical report is established to provide the industrial standards for Quad Flat I-Lead Package (hereinafter called QFI), to provide the design guidelines in producing the QFI and developing the automatic mounting machines and related parts.

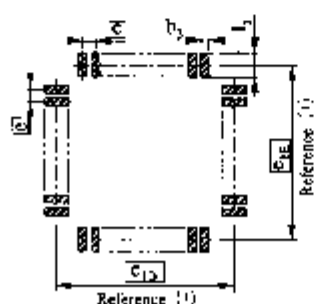
### 2. History of Discussion

This technical report succeeded **EIAJ ED-7409** [General Rules for the Preparation of Outline Drawings of Integrated Circuits Quad Flat I-Leaded Packages]. QFI is a square package with leads extending from each side of the body. The leads are formed in a I-shaped configuration (or butt lead) for surface mounting. The preparation of outline drawings of QFI was assigned in the work schedule of the 1987 term, and the relevant deliberations were started on the meeting of the Technical Committee on Semiconductor Package Outlines [the former committee of Technical Standardization Committee on Semiconductor Device Package] held May 1987. In its early stage, the discussions were focussed on adopting either the inch (mil) system or the millimeter (mm) system as the centerline-to-centerline spacing of the leads. Finally, it was decided to adopt the inch system from the standpoint of compatibility with PLCC, which is the conventional package. And three types of package, 50 mil, 40 mil and 30 mil were adopted as standard QFI. However, all dimensions were unified to the millimeter unit, with the mil unit as a reference unit. For the present, the maximum number of pins of QFI was limited to 100 and the variety of packages was limited to 10 types, because the committee took the limits of practical use into consideration when deciding the variation. The deliberations for package name were started by adopting the name "MSP". But latter on, it was decided, in response to suggestion asking for the use of clear-cut name taking into consideration the lead configuration, to adopt the unified nomenclature "QFI" including the meaning of the I-shaped configuration. The final deliberation was concluded in June 1988.

**EIAJ ED-7409** was abolished after the lapse of ten years in 1998. This technical report succeeding **ED-7409** was being deliberated as the design guideline by Plastic Subcommittee, which is one of subcommittees of Technical Standardization Committee on Semiconductor Device Package.

### 3. Reference drawing for QFI mounting pad (Unit mm)

Mounting pad layout of **EIAJ ED-7409** was changed to allowable range of soldering part of terminals. Mounting pad layout was moved to the comment section as a reference drawing.



e	b <sub>min</sub>	l <sub>min</sub>
1.270 (50mil)	0.65	1.5
1.016 (40mil)		
0.762 (30mil)	0.46	2.0

#### Note

- (1) Indicate the spacing between the true geometrical center positions of the pad rows.
- (2) Add other fine pitch packages according to the mounting technology.

**10. COMMITTEE MEMBERS**

The Plastic Package Subcommittee of the Technical Standardization Committee on Semiconductor Device Packages has mainly deliberated this technical report. The subcommittee members are shown below.

**<Technical Standardization Committee on Semiconductor Device Package>**

Chairman	Shozo Minamide	SHARP CORPORATION
----------	----------------	-------------------

**<Plastic Package Subcommittee>**

Chief	Kaoru Sonobe	NEC CORPORATION
Vice-Chief	Takashi Okada	TOSHIBA CORPORATION
Vice-Chief	Takahiro Imura	TEXAS INSTRUMENTS JAPAN LTD.
Members	Hideo Shimada	ENPLAS CORPORATION
	Kazuhiko Sera	OKI ELECTRIC INDUSTRY CO., LTD.
	Hideyuki Iwamura	SANYO ELECTRIC CO., LTD.
	Shuya Haruguchi	SHARP CORPORATION
	Takayuki Nagumo	SUMITOMO 3M, LTD.
	Akinori Hara	SEIKO EPSON CO.
	Toshihiko Nojiri	SONY CORPORATION
	Toshihiro Murayama	SONY CORPORATION
	Kenji Kanesaka	NIPPON FOUNDRY INC.
	Tunco Kobayashi	IBM JAPAN, LTD.
	Tsukasa Ito	AMP JAPAN, LTD.
	Hiroaki Hirao	SAMSUNG ELECTRICS JAPAN CO., LTD.
	Kenichi Kurihara	NEC CORPORATION
	Nobuo Sato	MOTOROLA LTD.
	Shinya Kanamitsu	HITACHI LTD.
	Nobuya Koike	HITACHI LTD.
	Masnoru Yoshimoto	FUJITSU LIMITED
	Osamu Hirohashi	FUJI ELECTRIC CO., LTD.
	Shigeki Sakaguchi	MATSUSHITA ELECTRICS CORPORATION
	Kou Shimomura	MITSUBISHI ELECTRIC CORPORATION
	Nanahiro Hayakawa	YAMAICHI ELECTRICS CO., LTD.
	Hitoshi Matsunaga	UNITECHNO INC.
	Kazumi Morimoto	ROHM CO., LTD.



Technical Report of Electronic Industries Association of Japan

***EIAJ EDR-7321***

**Design guideline of integrated circuits  
for Quad Flat I-lead packages  
(QFI)**

Established in February, 1999

Prepared by  
Technical Standardization Committee on Semiconductor Device Package

Published by  
Electronic Industries Association of Japan

5-13, Nishi-shimbashi 1-chome, Minato-ku, Tokyo 105-0003, Japan  
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