

Technical Report of Japan Electronics and Information Technology Industries Association

EIAJ EDR-7327

Design guideline of integrated circuits for Single Inline Package

Established in January, 2001

Prepared by

Technical Standardization Committee on Semiconductor Device Package

Published by

Japan Electronics and Information Technology Industries Association

5-13, Nishi-shimbashi 1-chome, Minato-ku, Tokyo 105-0003, Japan Printed in Japan

Technical Report of Japan Electronics and Information Technology Industries Association Design guideline of integrated circuits for Single Inline Package (SIP)

1. SCOPE OF APPLICATION

This technical report regulates outline drawings and dimensions of Plastic Single Inline Package (hereinafter referred to as SIP) whose terminal pitch is 2.54mm, and 17.78mm, among the packages classified as form C in **EIAJ ED-7300** (Recommended practice on General Rule for preparing standard outline drawings (integrated circuits) of semiconductor devices) **Note** This technical report is the revised edition of **EIAJ ED-7413**.

2. TERMS

The definition of the terms used in this technical report complies with **EIAJ ED-7300**. New terms define in the description of this report.

3. BACKGROUND

This technical report intended to standardize the outer dimensions of SIP, and ensure compatibility between products. It shows the standard design values on concept of the design centers as far as possible for standardization.

4. DEFINISION OF SIP

SIP is classified as FORM C in item 6 "Out line classification of the semiconductor package" of **EIAJ ED-7300**, and defined a package with T/H terminals which are led out of the longer side of itself in single direction and which are perpendicular to the surface of a PCB.

5. NUMBER OF TERMINALS

Number of terminals complies with the **EIAJ ED-7300**.

6. NOMINAL DIMENSIONS

The dimension of package height \times package length (Symbol: A2nom \times D1nom) is applied to nominal dimensions.

7. REFERENCE CHARACTERS AND DRAWING

7.1 Outline Drawings



- **Note** (¹) The mounting plane is the dimension that determined when the pins are completely inserted in the holes that are sized $\phi 0.8 \pm 0.05$ mm.
 - (²) The maximum material conditions apply to the positional tolerance of the terminals.
 - (³) The index mark indicates the pin NO.1. By the way, 1/2 or more of the area of this index mark must be contained within the hatched zone.
 - (⁴) The shape of the chamfer for visual index and mechanical index is arbitrary, by the shape shown in the figure below is recommended.
 - (⁵) The dimensions of terminal section apply to the ranges from 1.0mm to 1.5mm from the tip of terminals.



8. OUTER DIMENNSION

Table 1 below shows the standard dimension. Combination of the standard dimension shown below allow a number of package variation. IF packages are design newly, their dimensions shall be selected in the Table of Standard Package Dimension List in the Appendix.

8.1 Group 1

Description	Symbol	S	Standard		Recommended Values	Remarks
Nominal	$A_{2nom} \times$	Package height $A_{2nom} \times P$	ackage lengt	h D_{1nom} is applied to		
Dimensions	$D_{1\text{nom}}$	Nominal dimensions				
A	$_{2nom}=4.58e=2.$	54	Aanon	=4.58 e=1.778		
n	$A_{2nom} \times D_1$	-	<u>n</u>	$A_{2nom} \times D_{1nom}$		
	$Z_{1nom} = 0.88$	$Z_{1nom} = 1.270$		$Z_{1nom} = 1.270$	$Z_{1nom} = 2.159$	
5	4.58 × 11	93 4.58 × 14.47	5	4.58 × 9.65	1.58×11.43	
6	$4.58 \times 14.$	47 4.58 × 17.01	6	4.58 × 11.43	4.58×13.20	
7	4.58×17.1	01 4.58 × 19.55	7	4.58×13.20	4.58×14.98	
8	$4.58 \times 19.$	55 4.58 × 22.30	8	4.58×14.98	4.58×16.76	
9	4.58×22.5	30 4.58 × 24.63	9	4.58 × 16.76	4.58×18.54	
10) $4.58 \times 24.$	63 4.58 × 27.17	10	4.58 × 18.54	4.58×20.32	
11	$4.58 \times 27.$	17 4.58 × 29.71	11	4.58×20.32	4.58×22.09	
12	$2 4.58 \times 29.5$	71 4.58 × 32.46	12	4.58 × 22.09	4.58 × 23.87	
13	4.58×32.5	25 4.58 × 34.79	13	4.58 × 23.87	4.58 × 25.65	
14	4.58 × 34.	79 4.58 × 37.33	14	4.58 × 25.65	4.58×27.43	
	7.10 [] 0.4	- 4		7 12 1 1 779		
A	$_{2nom} = 7.12 e = 2.3$	54	A _{2non}	n = 7.12 e = 1.778		
n	$A_{2nom} \times D_1$		N	$A_{2nom} \times D_{1nom}$	7 2 150	
-	$Z_{1nom}=0.88$	$Z_{1nom} = 1.270$	<u> </u>	$Z_{1nom} = 1.270$	L _{1nom} =2.159	
5	7.12×11.	93 7.12 × 14.47	<u> </u>	7.12 × 9.65	7.12×11.43	
<u>6</u>	7.12×14.	47 7.12 × 17.01	<u> </u>	7.12×11.43	7.12 × 13.20	
<u>/</u>	7.12×17.	01 7.12 × 19.55		7.12 × 13.20	<u>/.12 × 14.98</u>	
8	7.12 × 19.	55 7.12 × 22.30	- <u>8</u>	7.12 × 14.98	7.12×16.76	
<u>9</u>	7.12 × 22.	30 7.12 × 24.63	<u> </u>	7.12 × 16.76	7.12 × 18.54	
10	$7.12 \times 24.$	63 7.12×27.17		7.12 × 18.54	7.12 × 20.32	
11	$7.12 \times 27.$	1/ /.12 × 29./1	<u> </u>	7.12 × 20.32	7.12 × 22.09	
$\frac{12}{12}$	$\frac{2}{7.12 \times 29}$	71 7.12 × 32.46	<u> </u>	7.12 × 22.09	1.12 × 23.87	
13	5 /.12 × 32.	25 7.12 × 34.79	13	7.12 × 23.87	7.12 × 25.65	
12	7.12 × 34.	79 7.12 × 37.33	14	7.12×25.65	7.12×27.43	
A	_{2nom} =9.66 e=2.5	54	A _{2non}	=9.66 e=1.778		
N	$A_{2nom} \times D_1$	nom	n	$A_{2nom} \times D_{1nom}$		
_	$Z_{1nom}=0.88$	$Z_{1nom} = 1.270$		$Z_{1nom} = 1.270$	Z _{1nom} =2.159	
5	9.66×11.5	93 9.66 × 14.47	5	9.66×9.65	0.66×11.43	
6	9.66×14.4	47 9.66 imes 17.01	6	9.66 × 11.43	0.66 × 13.20	
7	$9.66 \times 17.$	01 9.66 × 19.55	7	9.66 × 13.20	9.66 × 14.98	
8	9.66×19.1	55 9.66 × 22.30	8	9.66 × 14.98	9.66 × 16.76	
9	9.66×22.1	30 9.66 × 24.63	9	9.66 × 16.76	0.66 × 18.54	
10) $9.66 \times 24.$	63 9.66 × 27.17	10	9.66×18.54	0.66×20.32	
11	$9.66 \times 27.$	17 9.66 × 29.71		9.66×20.32	0.66×22.09	
12	9.66×29.7	71 9.66 × 32.46	12	9.66 × 22.09	0.66 × 23.87	
13	9.66×32.5	25 9.66 × 34.79	13	9.66 × 23.87	0.66×25.65	
14	$9.66 \times 34.$	79 9.66 × 37.33	14	9.66×25.65	0.66 × 27.43	

Table 1

Description	Symbol		Standard		Recommended Values	Remarks
Seated hight	А	A _{min} =A _{max} - 1.27				A _{max} is included
		NominalDimension $A_2 \times D_1$	A _{min}	A _{max}	_	resin
		$4.58 \times D_1$	4.83	6.10	_	buils.
		$7.12 \times D_1$	7.37	8.64	_	
		$9.66 \times D_1$	9.91	11.18	_	
Stand-off	A_1	A	_{1 min} = 0.51			
height		А	1 _{max} =2.54			
Package	A_2				$A_{2nom} = 4.58$	A ₂ is not
height		$A_{2max} = A_{max} - 0.51$			or	included
		$A_{2min} = A_{min} - 2.54$			7.12	resin burrs.
		NominalDimension $A_2 imes D_1$	A_{2min}	A _{2max}	or	
		$4.58 \times D_1$	3.56	5.59	9.66	
		$7.12 \times D_1$	6.10	8.13		
		$9.66 \times D_1$	8.64	10.67	_	
All	A_3					
Package		$A_{3max} = A_{max} + L_{max}$				
height		$A_{3\min} = A_{\min} + L_{\min}$				
		NominalDimension $A_2 \times D_1$	A _{3min}	A _{3max}	-	
		$4.58 \times D_1$	8.84	9.91	-	
		$7.12 \times D_1$	11.18	12.45	-	
		$9.66 \times D_1$	13.72	14.99		
					_	

Table 1 (continued)

Table 1 (continued)

Description	Symbol		Standard				Recommended Values		Rem	arks	
Terminal	b	b _{min}		b _{max}			S	older Plat	ing		
width		0.40		0.65				b _{nom} =0.52	2		
							Pd	Plating b _{no}	_m =0.5		
	b_1	b _{1min}		b _{1max}				b _{1nom} =0.5			
		0.40		0.65							
Terminal	b_2	b _{2min}		b _{2max}				b2nom=0.25	5		
end width		0		0.45							
Terminal	b ₃	b _{3min}		b _{3max}				b _{3nom} =1.27	7		
shoulder		0.85		2.00							
width	b₄	b _{4min}		b _{4max}				h. −1.0			
	04	0.85		2.00				04nom-1.0			
	C			C _{max}				1. D.	0.07		
Terminal	C	0.20		0.40			50	der P.c _{nom} =	:0.27		
thickness				C			Pd I	Plating C _{nom}	=0.25		
	c_1	0.20		$C_{1\text{max}}$				C _{1nom} =0.2	5		
		0.20		0.40							
Terminal	е	e=2.54									
pitch		P-1 778									
		<u>e</u> -1.770						- •			
Package	E	$E_{max}=3.8$	1					$E_{nom} = 2.8$	80		
width										-	
Package	D	$D_{max} = e \times$	(n-1)+2Z	max			D _{nom}	$= \mathbf{e} \times (\mathbf{n} -$	1)+2	Z_{nom}	
length		· e=2.	54				e=2.	54			
			D	n	D	n D				D _{nom}	
		5	15.24	10	27.94		_{iom} = 991	$Z_{nom} = 2.261$		$Z_{nom} =$ 0.991	$Z_{nom} = 2.261$
		6	17.78	11	30.48	5 12	2.142	14.682	10	24.842	27.382
		7	20.32	12	33.02	- 7 17	1.682 7.222	17.222	11	27.382 29.922	29.922 32.462
		$\frac{8}{9}$	22.86	13	35.56	8 19	0.762 2.302	22.302 24.842	13 14	32.462 35.002	35.002 37.542
			25.10	11	50.10			778			
		$\frac{\cdot e=1}{\dots}$	//8			- <u> </u>	6–1.	//8		D	
		<u> </u>	D_{max}	n 10	D _{max}	- Z _r	nom nom=	Z _{nom} =	-	D _{nom} Z _{nom} =	Z _{nom} =
		6	14.224	10	23.114	1. 5 9.	372 856	2.261 11.634	10	1.372 18.746	2.261 20.524
		7	16.002	12	24.892	6 11	.634	13.412	11	20.524	22.302 24.080
		8	17.78	13	26.670	8 15	5.190	16.969	13	24.080	25.858
		9	19.558	14	28.448	9 16	0.968	18.746	14	25.858	27.636
	D	D	(n-1)+27	,			D.		(n-1)	
	$\boldsymbol{\nu}_{\mathrm{l}}$		(II · I)⊤ ∠ L	'Imax				n-⊡ ^	(11-1		
								Inom			
		1					1			1	

Table 1 (continued)

Description	Symbol	Standard	Recommended Values	Remarks
Positional	х	x=0.25		
tolelance of				
terminal				
Number of	n	(a) Number of terminal are determined as follows.		
terminals		(b) The maximum number of pins that can be arranged		
		within the cage body should be "n".		
		(c) The actual number of pins may be smaller than "n".		
		It must be remembered, however, that the first and		
		the "n"th pins must exist without fail irrespective of		
		the actual number of pins.		

8.2 Group 2

		Table 2		unit: mm
Description	Symbol	Standard	Recommended Values	Remarks
Package	Ζ	· e=2.54	Z _{nom} =0.991	
overhang		$Z_{max} = e$	or	
		Z _{max} =2.54	2.261	
		· e=1.778	$Z_{nom} = 1.372$	
		$Z_{max} = 1.5 \times e$	or	
		Z _{max} =2.667	2.261	
	Z_1	· e=2.54	$Z_{1nom} = 0.889$	
		$Z_{1max} = e -0.203$	or	
		Z _{1max} =2.337	2.159	
		· e=1.778	$Z_{1nom} = 1.270$	
		$Z_1 m_{ax} = 1.5 \times e - 0.203$	or	
		Z _{1max} =2.464	2.159	
Terminal	L	L _{min} L _{max}	$L_{nom}=3.30$	
length		2.54 3.90		

9. STANDARD PACKAGE LIST

To further clarify the combinations of part dimensions, the combinations of recommended package classifications shall be indicated as shown below as assistance in the design and development of new packages in the future.

Nominal Dimensions Package height \times Package length $A_{2nom} \times D_{1nom}$					
n	A _{2nom} =4.58 e=2.	54	A _{2nom} =4.58 e=1.778		
11	Z _{1nom} =0.889	$Z_{1nom} = 1.270$	$Z_{1nom} = 1.270$	Z _{1nom} =2.159	
5	4.58×11.93	4.58×9.65	4.58×9.65	4.58×11.43	
6	4.58×14.47	4.58×11.43	4.58×11.43	4.58×13.20	
7	4.58×17.01	4.58×13.20	4.58×13.20	4.58×14.98	
8	4.58×19.55	4.58×14.98	4.58×14.98	4.58×16.76	
9	4.58×22.30	4.58×16.76	4.58×16.76	4.58×18.54	
10	4.58×24.63	4.58×18.54	4.58×18.54	4.58×20.32	
11	4.58×27.17	4.58×20.32	4.58×20.32	4.58×22.09	
12	4.58×29.71	4.58×22.09	4.58×22.09	4.58×23.87	
13	4.58×32.25	4.58×23.87	4.58×23.87	4.58×25.65	
14	4.58 × 34.79	4.58×25.65	4.58×25.65	4.58×27.43	

Table 2 STANDARD PACKAGE LIST

Nominal Dimensions Package height \times $\ Package length A_{2nom} \times D_{1nom}$

n	A _{2nom} =7.12 e=2.	54	A _{2nom} =7.12 e=1.778		
11	$Z_{1nom} = 0.889$	Z _{1nom} =1.270	Z _{1nom} =1.270	Z _{1nom} =2.159	
5	7.12×11.93	7.12×14.47	7.12×9.65	7.12×11.43	
6	7.12×14.47	7.12×17.01	7.12×11.43	7.12×13.20	
7	7.12×17.01	7.12×19.55	7.12×13.20	7.12×14.98	
8	7.12×19.55	7.12×22.30	7.12×14.98	7.12×16.76	
9	7.12×22.30	7.12×24.63	7.12×16.76	7.12×18.54	
10	7.12×24.63	7.12×27.17	7.12×18.54	7.12×20.32	
11	7.12×27.17	7.12×29.71	7.12×20.32	7.12×22.09	
12	7.12×29.71	7.12×32.46	7.12×22.09	7.12×23.87	
13	7.12 × 32.25	7.12×34.79	7.12×23.87	7.12×25.65	
14	7.12×34.79	7.12×37.33	7.12×25.65	7.12×27.43	

Nominal Dimensions Package height \times Package length $A_{2nom} \times D_{1nom}$

n	A _{2nom} =9.66 e=2.54		A _{2nom} =9.66 e=1.778		
	Z _{1nom} =0.889	Z _{1nom} =1.270	Z _{1nom} =1.270	Z _{1nom} =2.159	
5	9.66 × 11.93	9.66×14.47	9.66×9.65	9.66×11.43	
6	9.66×14.47	9.66×17.01	9.66×11.43	9.66×13.20	
7	9.66×17.01	9.66×19.55	9.66×13.20	9.66×14.98	
8	9.66×19.55	9.66×22.30	9.66×14.98	9.66×16.76	
9	9.66×22.30	9.66×24.63	9.66×16.76	9.66×18.54	
10	9.66 × 24.63	9.66×27.17	9.66×18.54	9.66×20.32	
11	9.66×27.17	9.66×29.71	9.66×20.32	9.66×22.09	
12	9.66×29.71	9.66×32.46	9.66×22.09	9.66×23.87	
13	9.66 × 32.25	9.66 × 34.79	9.66×23.87	9.66×25.65	
14	9.66 × 34.79	9.66×37.33	9.66×25.65	9.66 × 27.43	

10. STANDARD RESISTRATION

When you need to resister a new outline specification on the standard, complete the appendix format 5 in The Standardization Committee on Semiconductor Device Package steering rule, in compliance with the Standardization Rule.

In order to make a package dimension table, which comes under Item 2, Appendix format 5, fill the dimensions marked with (\lor) in the following **Table 3**.

Serial Number				
Externa	al Type	P-SIPOOO-OOO	00×0000-0.00	
Reference	Reference Symbol		nom	max
	А	arphi		\checkmark
	A_1	\checkmark		\checkmark
	A_2	\checkmark		\checkmark
	A_3			\checkmark
	b	\checkmark		\checkmark
	b ₁			
	b_2			\checkmark
1	b ₃			\checkmark
Gtoup	b_4			\checkmark
	с			
	c ₁			
	е			
	Е			\checkmark
	D			\checkmark
	D_1			\checkmark
	Х			\checkmark
	n			
b	Z			
ltou 2	Z_1			\checkmark
G	L			\checkmark

Table	3
-------	---

11. RERATED STANDARD

(1)	EIAJ ET-9001	"Rules for the drafting and presentation of EIAJ Standards"
(2)	EIAJ ED-7300	"Recommended practice on General Rules for the preparation of outline
		drawings of semiconductor packages"
(3)	EIAJ ED-7301	"Manual for the standard of integrated circuits package"
(4)	EIAJ ED-7302	"Manual for integrated circuits package design guide"
(5)	EIAJ ED-7303	"Names and code for integrated circuits package"

EXPLANATORY NOTE

1. OBJECTIVES OF THE TECHNICAL REPORT

This technical report has been prepared to show the industry standard and offer design guideline when developing the Plastic Single Inline Package (hereinafter referred as SIP), and related parts. Electronic Industries Association of Japan (EIAJ) and The Japan Electronic Development Association (JEIDA) have marged effective November 1,2000, the Japan Electronics and Information Technology Industries Association (JEITA).

2. HISTORY OF REVIEW

EIAJ ED-7413 would be abolished by the laps of ten years in 1999. Therefore, it was reviewed by Peripheral Package Subcommittee under "Technical Standardization Committee on Semiconductor Device Package" and was issued as design guideline.

3. KEY POINT FOR REVIEW

(1) Datum marking

The datum and geometrical tolerance were adopted from this technical report.

(2) Definitions of dimension

The recommended values in this technical report were adopted the values **EIAJ ED-7413** as far as possible. And a format was changed in according to **EIAJ ED-7302** "Manual for the Standard of integrated circuits package".

The dimension of package height \times package length (Symbol: A2nom \times D1nom)is applied to nominal dimensions. And terminal width before treatment (b1) and terminal thickness before treatment (c1) was newly shown and Pd plating is added.

(3) Standard package list

Standard package list was added in according to **EIAJ ED-7302**. The Package which were produced in 1999 were registered as standard packages.

(4) Standard registration

Standard registration list was added in according to EIAJ ED-7302.

4. Background for the respective dimensional rules

(1) Terminal width

b in **EIAJ ED-7413** meant terminal width after treatment so it was replaced to b_1 in this technical report. Terminal width before treatment was defined as b. Because terminal width before treatment (b_1) was added, b_1 , b_2 in **EIAJ ED-7413** replace b_2 , b_3 respectively. Pd plating was added to the exterior plating.

(2) Terminal thickness

c in the **EIAJ ED-7413** meant terminal thickness after treatment, so it replaced c_1 in this technical report. Terminal thickness before treatment was defined to as c. Pd plating was added to the exterior treatment. Solder dipping was deleted because of less possibility of adoption.

5. COMMITTEE MENBERS

This technical report has been discussed by the Peripheral Package Subcommittee of the Technical Standardization Committee on semiconductor Device Packages. The members are as shown below.

<Technical Standardization Committee on Semiconductor Device Packages> Chairman ELPIDA MEMORY, INC. Ichiro Anjo <Peripheral Package Subcommittee> Chairman SANYO ELECTRIC CORP. Hideyuki Iwamura Co-chairman TOSHIBA CORP. Yasuhiro Koshio MATSUSHITA ELECTRONICS CORP. Co-chairman Toshiyuki Fukuda ENPLAS CORP. Hideo Shimada OKI ELECTRONICS INDUSTRY CO., LTD Kazuhiko Sera KYOCERA CORP. Yoshihiro Nabe SHARP CORP. Hideya Haruguchi SUMITOMO 3M CORP. Takayuki Nagumo SEIKO EPSON CORP. Yoshiaki Emoto SONY CORP. Nobuhisa Ishikawa IBM JAPAN CORP. Tuneo Kobayashi SAMSUNG ELECTORONICS JAPAN CO., LTD. Hiroaki Hirao NEC CORP. Kaoru Sonobe NGK CORP. Katsuaki Sugino HITACHI LTD. Takahiro Naito FUJITSU LTD. Kaoru Tachibana FUJI ELECTRIC CO.,LTD. Osamu Hirose MITSUBISHI ELECTRIC CORP. Kou Shimomura YMAICHI ELECTRIC CO.,LTD. Nobuo Kawamura UNITECHNO INC. Hitoshi Matsunaga ROHM Co.,LTD. Masahiro Tsuji ON SEMICONDUCTOR Ryo Sugawara